

5 Volt Logic Product Spectrum

FEATURES AND FUNCTIONS

HEF4000	HC/T	AHC/T	FAST	ABT	CBT
<ul style="list-style-type: none"> • 90 ns performance* • ±3 mA drive* • 600 µA standby current • V_{CC}: 5-15V • Gate, MSI, buffer functions • Multisourced 	<ul style="list-style-type: none"> • 9 ns performance • ± 8 mA drive • 80 µA standby current • V_{CC}: 2-6V • Gate, MSI, buffer functions • Analog switch functions • PicoGate and DQFN packaging • Multisourced 	<ul style="list-style-type: none"> • 5 ns performance • ± 8 mA drive • 40 µA standby current • V_{CC}: 2-6V • Gate, bus interface functions • PicoGate packaging • Multisourced • Replaces VHC/T 	<ul style="list-style-type: none"> • 4 ns performance • -15/24 mA drive • 90 mA standby current • V_{CC}: 4.5-5.5V • Termination resistor option • Gate, MSI, bus interface functions • Multisourced 	<ul style="list-style-type: none"> • 3 ns performance • -32/64 mA drive • 250 µA standby current • V_{CC}: 4.5-5.5V • Bus hold option • Termination resistor option • Live insertion • Gates, bus interface functions • Multisourced 	<ul style="list-style-type: none"> • Sub 1 ns prop delays • 5 Ω R_{ON} • V_{CC}: 4.5-5.5V • For circuit isolation and switching • Precharge circuit for hot plugging • Schottky or charge pump undershoot protection • Internal diode for level shifting • Multisourced

* At 15V_{VCC}

Low Voltage Logic Product Spectrum

FEATURES AND FUNCTIONS

LV	LVC	ALVC	LVT	ALVT	AVC
<ul style="list-style-type: none"> • 9 ns performance • ± 8 mA drive • 20 µA standby current • V_{CC}: 1-3.6V* • Gate, MSI, buffer functions • Multisourced 	<ul style="list-style-type: none"> • 4 ns performance • ± 24 mA drive • 20 µA standby current • V_{CC}: 1.2-3.6V • 5V tolerant I/O's • Live insertion • Bus hold option • Termination resistor option • Gates, MSI, 8/16/32-bit bus interface functions • PicoGate LFBGA, DQFN packaging • Multisourced • Replaces LCX 	<ul style="list-style-type: none"> • 2 ns performance • ± 24 mA drive • 40 µA standby current • V_{CC}: 1.2-3.6V • 5V tolerant inputs** • Bus hold option • Termination resistor option • Gates, 8, 16, and 32-bit bus interface functions • DQFN and LFBGA packaging • Multisourced • Replaces VCX 	<ul style="list-style-type: none"> • 2 ns performance • -32/64 mA drive • 120-190 µA standby current • V_{CC}: 2.7-3.6V • 5V tolerant I/O's • Live insertion • Built-in bus hold • Termination resistor option • Gates, 8/16/32-bit bus interface functions • DQFN and LFBGA packaging • Multisourced 	<ul style="list-style-type: none"> • 1.5 ns performance • -32/64 mA drive • 90 µA standby current • V_{CC}: 2.3-3.6V • 5V tolerant I/O's • Live insertion • Built-in bus hold • Termination resistor option • Bus interface functions • Multisourced 	<ul style="list-style-type: none"> • 1.0 ns performance • ± 8 mA static drive • High dynamic drive • 20 µA standby current • Optimized for 2.5V • V_{CC}: 1.2-3.3V • 3.6V tolerant I/O's • Bus hold option • Termination resistor option • Bus interface functions • Multisourced

* LV : some functions can operate up to 5.5V

** Only on non-bus hold part types

FUNCTIONAL MATRIX

	Functions													Special Features					Process					
	Buffers/Line Driver	Flip Flops	Counters	Shift Registers	Encoders/Multiplexers	Decoder/Demultiplexers	Comparators/Parity Generators	Arithmetic	Gates	Analog Switches	Transceivers	FIFOs	Level Shifters	Phase Lock Loop	Bus Hold	Series Damping Resistors	Live Insertion	5V Tolerant I/O's	Input Disable Circuit	Power Up Reset	Bipolar	CMOS	BiCMOS	
3.3 Volt Logic																								
LV	•	•	•	•	•	•			•	•	•													
LVC	•	•	•		•	•			•		•		•		•	•		•				•		
ALVC	•	•									•		•		•	•						•		
LVT	•	•							•		•				•	•	•	•		•				•
ALVT	•	•									•				•	•	•	•		•				•
5 Volt Logic																								
HEF4000	•	•	•	•	•	•	•	•	•	•	•			•									•	
HC/HCT	•	•	•	•	•	•	•	•	•	•	•		•	•									•	
FAST	•	•	•	•	•	•	•	•	•	•	•				•						•			
ABT	•	•							•		•				•	•	•		•	•				•
MULTIBYTE	•	•									•						•		•	•				•
AHC/AHCT	•	•		•	•	•			•		•											•		

Product naming conventions: logic

Example

Below is a fictional example using all designators in the correct order. Descriptions of each designator follows.

N **74** **LVC** **T** **H** **32** **2** **244** **A** **-1** **D**

Required (Always Present)

Optional (Present Only When Relevant)

N **Temperature Range**
 'N'
 Indicates commercial temperature range for the FAST product family.

'I'
 Indicates industrial temperature range for the FAST product family.

'P'
 Stands for Philips on HC/T products.

74 **Temperature Range**
 '74'
 Indicates the device is either commercial or industrial grade.
Note: No designator is used for HEF4000 devices.

LVC **Family**
 Designators indicate the device family. For a complete list of the available Philips Logic product families please see page 2.6.

T **TTL Voltage Level**
 'T'
 Indicates TTL switching levels on HC and AHC devices. When no designator is present this indicates CMOS switching levels on HC and AHC devices.
Note: For all other families (except HEF4000 and FBL where TTL switching levels are not available) TTL switching levels are standard. Consequently, no TTL designator is used for these families.

H **Bus Hold**
 'H'
 Indicates the presence of the bus hold feature on ABT(16),AVC,ALVC, and LVC(16) devices. On these devices bus hold capability is optional.
Note: Bus hold is standard on LVT and ALVT bus interface devices. Consequently, no bus hold designator is used.

32 **Bit Width**
 '1G' '2G' '3G'
 Indicates single-, dual-, and triple-gate devices.
 'Blank'
 Indicates the device falls within the 8-bit range (the range includes 1, 2, 3, 4, 6, 8, 9, and 10 bit devices). 8-bit devices are offered in the ABT,ALVC, FAST, FBL, HC/T, HEF4000, LV, LVC, and LVT families.
 '16'
 Indicates the device falls within the 16-bit range (the range includes 16, 18, and 20 bit devices). 16-bit devices are offered in the ABT,ALVC,AVC, LVC, and LVT families.
 '32'
 Indicates the device falls within the 32-bit range (the range includes 32, and 36 bit devices). 32-bit devices are offered in the ALVC and LVC families.

2 **Output Termination**
 '2'
 Indicates the presence of optional output series termination. This option is available in the ABT,ALVC, FBL, LVC, and LVT families.

244 **Function**
 Designators indicate the device function. A complete list of the functions is available on our website www.semiconductors.philips.com/logic.

A **Device Version**
 'A' 'B' 'C' etc.
 Designators usually indicate a revision or new version. Please check the part's datasheet to determine the specific meaning of the associated designator for this field.

-1 **Termination Resistors**
 '-1'
 Indicates the presence of terminations resistors for products in the FAST family.

D **Package**
 Designators indicate the device package. For a complete list of the available Philips Logic packages and designators please visit our Family-to-Package Matrix page.

Additional Designators

In North America, the following additional designators may be added to the end of the product name:

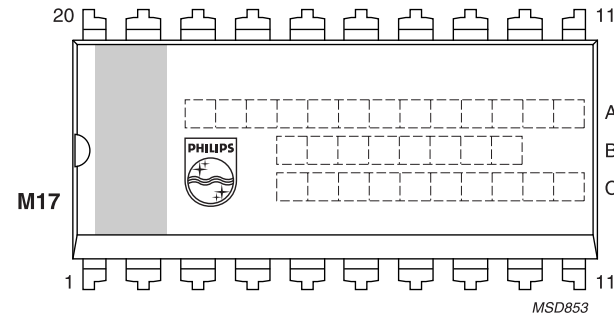
'-T'
 Standard tape and reel packing method

'-G'
 Smaller tape and reel packing method

'-S'
 Single tray packing method

Product Marking

This illustration depicts the top-side marking of a 20-pin SO package. This will be used as a basis to explain the top-side marking.



Line A: Product Code (for example: "74HC541D")

Line B: Diffusion lot-ID or traceability code (e.g. "B38090ME")

Line C: Manufacturing Code + Die revision (e.g. "HRn9744 F")

Manufacturing Code:

- The first 3 digits ("HRn") indicate the "Diffusion", "Assembly" and "Final-Test" centers, respectively.
- The 4 number ("9744") indicate the assembly date-code in the form of a week-code (YYWW).

Die Revision:

("F") is an alpha-numeric code representing the revision of the silicon die contained within the package.

For package outlines, please refer to the package overview at the end of this guide.

STANDARD PACKING QUANTITIES

Package Code	Pin Count	Quantities		
		Devices Per Tube	Devices Per Box	Devices Per Reel (-G / -T)
SMALLER PACKAGE (PICOGATE LOGIC)				
GW	5	--	--	3000/10000
GW	6	--	--	3000
DC	8			
SMALLER PACKAGE (PICOGATE LOGIC)				
GV	5			3000
GV	6			3000
DP	8			3000
DUAL IN-LINE (DIL)				
N	14	25	1000	--
N	16	25	1000	--
N	18	22	880	--
N	20	18	720	--
N	24	15	600	--
N	28	13	312	--
N	40	9	216	--
PLASTIC LEADED CHIP CARRIER (PLCC)				
A	28	34	2176	750
A	44	26	1300	500
A	68			
A	80			
A	84	15	420	250
PLASTIC SMALL OUTLINE (SOL)				
D	8	100	2000	2500
D	14	57	1140	2500
D	16	50	1000	2500
D	16 (Large)	48	1920	1000
D	20	38	1520	2000
D	24	30	1200	1000
D	28	27	1080	1000
SHRINK SMALL OUTLINE (SSOP)				
DB	14	78	1092	2000
DB	16	78	1092	2000
DB	20	66	924	1000
DB	24	59	826	1000
DL	48	31	1581	1000
DL	56	26	1326	1000
THIN SHRINK SMALL OUTLINE (TSSOP)				
DP	8			2500
PW	14	96	2400	2500
PW	16	96	2400	2500
PW	20	75	1875	2500
PW	24	63	1575	2500
DGG	48	39	975	2000
DGG	56	35	875	2000
PQFP (PLASTIC QUAD FLAT PACK)				
BB	52	96 (per single tray)	480 (per 5 tray)	500
LFBGA (LOW PROFILE BALL GRID ARRAY)				
EC	96 ball	285 (per single tray)	1425 (per 5 tray)	3500
EC	114 ball	240 (per single tray)	1200 (per 5 tray)	3500
VFBGA (Plastic Very Thin Fine-Pitch Ball Grid Array)				
EV	56 ball	1820 (per single tray)	9100 (per 5 tray)	3500
LQFP (LOW PROFILE QUAD FLAT PACK)				
BD	32	250 (per single tray)	1250 (per 5 tray)	2000
B	48	250 (per single tray)	1250 (per 5 tray)	2000
B	52	96 (per single tray)	480 (per 5 tray)	500
B	64	160 (per single tray)	800 (per 5 tray)	--
B	80	119 (per single tray)	595 (per 5 tray)	1000
DQFN (DEPOPULATED QUAD FLAT PACK NO LEADS)				
BS	14			3000
BS	16			3000
BS	20			3000
BS	24			3000

5 VOLT LOGIC LEVELS

Output Type	TTL	CMOS	ETL	BTL	PECL
	PP	PP	PP	OC	Diff
VCC	5	5	5	5	5
VOH	2.4	4.5	2.4	2.1	4
VIH	2	3.5	1.6	1.62	3.82
VT	1.5	2.5	1.5	1.55	3.595
VIL	0.8	1.5	1.4	1.47	3.37
VOL	0.4	0.5	0.4	1.1	3.19

ETL = Enhanced Transceiver Logic
 PECL = Pseudo ECL (or Positive-referenced ECL)
 BTL = Backplane Transceiver Logic (Futurebus/Pibus standard)
 PP = Push-Pull
 OC = Open Collector
 Diff = Differential

LOW VOLTAGE LOGIC LEVELS

Output:	LVTTTL	LVPECL	BTL	LVDS	GTL ¹	GTL+	GTL	HSTL	SSTL 3 ¹	SSTL 2 ²
	PP	Diff	OC	Diff	OD	OD	OD	PP	Diff	Diff
VCC	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3	2.5
VOH	2.4	2.275	2.1	1.45	1.5	1.5	1.2	1.1	2.1/2.3	1.82/2.01
VIH	2	2.1075	1.62	1.3	1.1	1.2	0.85	0.85	1.9	1.7
VT	1.5	1.8825	1.55	1.2	1	1	0.8	0.75	1.5	1.25
VIL	0.8	1.6575	1.47	1.1	0.9	0.8	0.75	0.65	1.1	0.8
VOL	0.4	1.49	1.1	0.95	0.65	0.5	0.4	0.4	0.9/0.7	0.68/0.49

All numbers are in volts
 Notes 1 & 2: Dual voltages represent Class 1/Class 2 output drive classifications

LV TTL = Low Voltage TTL
 LVPECL = Low Voltage PECL
 BTL = Backplane Transceiver Logic
 LVDS = Low Voltage Differential Signalling
 GTL = Gunning Transceiver Logic
 GTL+ = Intel GTL version
 GTLP = GTL Plus
 HSTL = High Speed Transceiver Logic
 OC = Open Collector
 OD = Open Drain
 Diff = Differential

2.5/1.8 VOLT CMOS SWITCHING LEVELS

VCC	VT	VOH	VIH	VOL	VIL
2.5 V	1.25 V	1.9 V	1.7 V	0.3 V	0.7 V
1.8 V	0.9 V	1.2 V	0.65xVCC	0.45 V	0.35xVCC

PICOLOGIC LOGIC PRODUCT NAMING COMPETITIVE CROSS REFERENCE

Family	Device Type	Philips	TI	Fairchild	ON Semi	Toshiba	ST Micro
HC(T) Series							
	Single Gate	74HC(T)1Gxxx		NC7S(T)xxx	MC74HC1Gxxx	TC7Sxxx	74H1G66
	Dual Gates	74HC(T)2Gxxx				TC7W(T)xxx	
	Triple Gates	74HC(T)3Gxxx				TC7W(T)xxx	
AHC(T) Series							
	Single Gate	74AHC(T)1Gxxx	SN74AHC(T)1Gxxx		MC74VHC1Gxxx	TC7S(H/ET)xxx	74V1G/Txxx
	Dual Gates	74AHC(T)2Gxxx				TC7WHxxx	74V2G/Txxx
	Triple Gates	74AHC(T)3Gxxx				TC7WHxxx	74V3G/Txxx
LVC Series							
	Single Gate	74LVC1Gxxx	SN74LVC1Gxxx	NC7SZxxx	NL17SZxxx	TC7SZxxx	74LX1G/Txxx
	Dual Gates	74LVC2Gxxx	SN74LVC2Gxxx	NC7WZxxx	NL27WZxxx		
	Triple Gates	74LVC3Gxxx	SN74LVC3Gxxx	NC7NZxxx	NL37WZxxx		
AUC Series							
	Single Gate	74AUC1Gxxx	SN74AUC1Gxxx	NC7SVxxx			
	Dual Gates	74AUC2Gxxx	SN74AUC2Gxxx	NC7WVxxx			
	Triple Gates	74AUC3Gxxx	SN74AUC3Gxxx	NC7NVxxx			

PICOLOGIC LOGIC PACKAGE DESIGNATION COMPETITIVE CROSS REFERENCE

Package	No. of pins	Philips	TI	Fairchild	ON Semi	Toshiba	ST Micro
SOT353	5	GW	DCK	P5	DFT	FU	CTR
SOT753	5	GV	DBV	M5	DTT	F	STR
SOT363	6	GW	DCK	P6	DFT	FU	
SOT457	6	GV	DBV		DTT	F	
MicroPak	6	GM		L6			
SOT765	8	DC	DCU	K8	US	FK	
SOT505-2	8	DP	DCT			FU	STR

COMPETITOR PACKAGE DESIGNATOR CROSS REFERENCE

Package	Pins	Philips	TI	Fairchild	ON Semi	Toshiba	Exar
DIP	14, 16, 20, 24, 28, 40, 48	N	N	N/PC/SPC	N	P	P
SOIC	14, 16	D	D	M/SC	D	FN	
SOIC	20, 24, 28	D	DW	WM/SC	DW	FW	
SSOP	14, 16, 20, 24	DB	DB	MSA	SD	FS	
SSOP	48, 56	DL	DL	MEA			
TSSOP-8	8	DP	DCT			FU	
TSSOP	14, 16, 20, 24	PW	PW	MTC	DT	FT	
TSSOP	48, 56	DGG	DGG	MTD	DT	FT	
PLCC	28, 44, 52, 68, 84	A					J
LQFP	32, 44, 48, 64, 80, 100	B, BE, BD	T, M				Q
LFBGA	96/114	EC	GKE/GKF				
VFBGA	48/56	EV					
DQFN	14, 16, 20, 24	BS					
SOT353	5	GW	DCK	P5	DFT	FU	
SOT753	5	GV	DBV	M5	DTT	F	
SOT363	6	GW	DCK	P6	DFT	FU	
SOT457	6	GV	DBV		DTT	F	
SOT505-2	8	DP	DCT			FU	

Note:
 Fairchild Semiconductor has different package designators for different families.

Logic Competitive Cross

LOGIC PRODUCTS COMPETITIVE CROSS REFERENCE

Family	Package	Philips	TI	Fairchild	ON Semi	Toshiba	ST Micro
CMOS							
4000B	DIP	HEF4xxxBPN	CD4xxxBE	CD4xxxBN	MC14xxxBP	TC4xxxBP	HCF4xxxBEY
	SOIC	HEF4xxxBTD	CD4xxxBM	CD4xxxBM/WM	MC14xxxBD	TC4xxxBFN	HCF4xxxBM1/M013TR
	SSOP I					TC4xxxFS	
	SSOP II	HEF4xxxDB					
HC(T) T=TTL	TSSOP			CD4xxxMTC	MC14xxxDT		
	DIP	74HC(T)xxxN	SN74HC(T)xxxN	MM74HC(T)xxxN	MC74HC(T)xxxN	TC74HC(T)xxxAP	M74HCxxxB1R
	SOIC	74HC(T)xxxD	SN74HC(T)xxxD/DW	MM74HC(T)xxxM/WM	MC74HC(T)xxxD	TC74HC(T)xxxAFW	M74HCxxxM1R/RM13TR
	SSOP II	74HC(T)xxxDB	SN74HC(T)xxxDB				
	TSSOP	74HC(T)xxxPW	SN74HC(T)xxxPW	MM74HC(T)xxxMTC	MC74HC(T)xxxDT		M74HCxxxTTR
AHC(T) T=TTL	DQFN	74HC(T)xxxBQ		74HC(T)xxxBQ			
	SOIC	74AHC(T)xxxD	SN74AHC(T)xxxD/DW	MM74VHC(T)xxxM/WM	MC74VHC(T)xxxD	TC74VHC(T)xxxAFN/FW	74VHCxxxTTR/ATTR
	TSSOP	74AHC(T)xxxPW	SN74AHC(T)xxxPW	MM74VHC(T)xxxMTC	MC74VHC(T)xxxDT	TC74VHC(T)xxxAFT	74VHCTxxxTTR/ATTR
Low Voltage CMOS							
LVC(H) H=bushold Feature	SOIC	74LVC(H)xxxAD	SN74LVC(H)xxxAD/DW	74LVCxxxM/WM	MC74LVCxxxD	TC74LVCxxxFN/FW	74LVCxxxM/MTR
	SSOP II	74LVC(H)xxxADB	SN74LVC(H)xxxADB	74LVCxxxMSA	MC74LVCxxxSD		
	TSSOP I	74LVC(H)xxxAPW	SN74LVC(H)xxxAPW	74LVCxxxMTC	MC74LVCxxxDT	TC74LVCxxxFT	74LVCxxxTTR
	DQFN	74LVC(H)xxxBQ		74LVCxxxBQ			
	SSOP III	74LVC(H)16xxxADL	SN74LVC(H)16xxxADL	74LVC16xxxMEA			
	TSSOP II	74LVC(H)16xxxADGG	SN74LVC(H)16xxxADGG	74LVC16xxxMTD	MC74LVC16xxxDT	TC74LVC16xxxFT	74LVC(H)16xxxM/MTR
ALVC(H)	LFBGA	74LVC(H)32xxxAEC	SN74LVC(H)32xxxAGKE				
	VFBGA	74LVC(H)16xxxAEV	SN74LVC(H)16xxxAGQL				
	SO	74ALVCxxxD	SN74ALVCxxxD/DW	74VCxxxM			
	TSSOP	74ALVCxxxPW	SN74ALVCxxxPW	74VCxxxMTC			
	DQFN	74ALVCxxxBQ		74VCxxxBQ			
	SSOP III	74ALVC(H)16xxxDL	SN74ALVC(H)16xxxDL	74VCX16xxxMEA			
LV	TSSOP II	74ALVC(H)16xxxDGG	SN74ALVC(H)16xxxDGG	74VCX16xxxMTD		TC74VCX16xxxFT	74VCX(H)16xxxTTR
	LFBGA	74ALVC(H)32xxxEC	SN74ALVC(H)32xxxGKE				
	SOIC	74LVxxxD	SN74LVxxxD/DW	74LVxxxM/WM	MC74LVxxxD	TC74LVxxxFN/FW	
AUC	SSOP II	74LVxxxDB	SN74LVxxxDB	74LVxxxMSA		TC74LVxxxFS	
	TSSOP I	74LVxxxPW	SN74LVxxxPW	74LVxxxMTC	MC74LVxxxDT	TC74LVxxxFT	
	TSSOP II	74AVC16xxxDGG	SN74AVC16xxxDGG				
BiCMOS							
ABT(H) H=bushold Feature	DIP	74ABTxxxN	SN74ABTxxxN	74ABTxxxPC			
	SOIC	74ABTxxxD	SN74ABTxxxD/DW	74ABTxxxSC			
	SSOP II	74ABTxxxDB	SN74ABTxxxDB	74ABTxxxMSA			
	TSSOP	74ABTxxxPW	SN74ABTxxxPW	74ABTxxxMTC			
	SSOP III	74ABT(H)16xxxDL	SN74ABT(H)16xxxDL	74ABT16xxxSSC			
	TSSOP II	74ABT(H)16xxxDGG	SN74ABT(H)16xxxDGG	74ABT16xxxMTD			
Low Voltage BiCMOS							
LVT Philips – Bushold is built in	SOIC	74LVTxxxD	SN74LVTHxxxD/DW	74LVTHxxxM/WM			
	SSOP II	74LVTxxxDB	SN74LVTHxxxDB	74LVTHxxxMSA			
	TSSOP	74LVTxxxPW	SN74LVTHxxxPW	74LVTHxxxMTC			
	DQFN	74LVTxxxBQ					
	SSOP III	74LVT16xxxDL	SN74LVTH16xxxDL	74LVTH16xxxMEA			
	TSSOP II	74LVT16xxxDGG	SN74LVTH16xxxDGG	74LVTH16xxxMTD			
ALVT (Bushold is Built in)	SSOP III	74ALVT16xxxDL	SN74ALVT16xxxDL				
	TSSOP II	74ALVT16xxxDGG	SN74ALVT16xxxDGG				
BIPOLAR							
FAST	DIP	N74FxxxN	SN74FxxxN	74FxxxPC/SPC			
	SOIC	N74FxxxD	SN74FxxxD/DW	74FxxxSC			
	SSOP II	N74FxxxDB	SN74FxxxDB	74FxxxMSA			

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	R _{ON}
74AHC1G66	PicoGate	5	5V Single Bilateral Switch	Yes	CMOS	5	-40 to +125	Vis to Vos	0.6	15
74AHCT1G66	PicoGate	5	5V Single Bilateral Switch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	0.7	15
74HC1G66	PicoGate	5	5V Single Bilateral Switch	Yes	CMOS	5	-40 to +125	Vis to Vos	2.0	
74HCT1G66	PicoGate	5	5V Single Bilateral Switch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	3.0	
74HC2G66	TSSOP	8	5V Dual Analog Switch	Yes	CMOS	5	-40 to +125	Vis to Vos	1.5	41
74HCT2G66	TSSOP	8	5V Dual Analog Switch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	2.0	41
74HC4016	DIL, SO, TSSOP	14	5V Quad Bilateral Switch	Yes	CMOS	5	-40 to +125	Vis to Vos	5.0	70
74HCT4016	DIL, SO	14	5V Quad Bilateral Switch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	6.0	70
74HC4051	DIL, SO, SSOP, TSSOP, DQFN	16	5V 8-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	5	-40 to +125	Vis to Vos	4.0	60
74HCT4051	DIL, SO, SSOP, TSSOP, DQFN	16	5V 8-Channel Analog Multiplexer/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	5.0	60
74HC4052	DIL, SO, SSOP, TSSOP, DQFN	16	5V Dual 4-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	5	-40 to +125	Vis to Vos	4.0	60
74HCT4052	DIL, SO, SSOP, DQFN	16	5V Dual 4-Channel Analog Multiplexer/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	5.0	60
74HC4053	DIL, SO, SSOP, TSSOP, DQFN	16	5V Triple 2-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	5	-40 to +125	Vis to Vos	4.0	60
74HCT4053	DIL, SO, SSOP, TSSOP, DQFN	16	5V Triple 2-Channel Analog Multiplexer/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	5.0	60
74HC4066	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad Bilateral Switch; Low "ON" Resistance	Yes	CMOS	5	-40 to +125	Vis to Vos	3.0	35
74HCT4066	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad Bilateral Switch; Low "ON" Resistance; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	3.0	35
74HC4067	DIL, SO, SSOP, TSSOP	24	5V 16-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	5	-40 to +125	Vis to Vos	7.0	60
74HCT4067	DIL, SO, SSOP, TSSOP	24	5V 16-Channel Analog Multiplexer/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	9.0	60
74HC4316	DIL, SO, SSOP, TSSOP	16	5V Quad Bilateral Switch with Logic Level Translation	Yes	CMOS	5	-40 to +125	Vis to Vos	5.0	80
74HCT4316	DIL, SO, SSOP, TSSOP	16	5V Quad Bilateral Switch with Logic Level Translation; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	6.0	80
74HC4351	DIL, SO, SSOP	20	5V 8-Channel Analog Multiplexer/Demultiplexer with Latch	Yes	CMOS	5	-40 to +125	Vis to Vos	4.0	60
74HCT4351	DIL, SO, SSOP	20	5V 8-Channel Analog Multiplexer/Demultiplexer with Latch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	6.0	60
74HC4353	DIL, SO	20	5V Triple 2-Channel Analog Multiplexer/Demultiplexer with Latch	Yes	CMOS	5	-40 to +125	Vis to Vos	4.0	60
74HCT4353	DIL, SO	20	5V Triple 2-Channel Analog Multiplexer/Demultiplexer with Latch; TTL Enabled	Yes	TTL	5	-40 to +125	Vis to Vos	6.0	60
HEF4016B	DIL, SO	14	Quad Bilateral Switch	Yes	CMOS	15	-40 to +125	Vis to Vos	5.0	115
HEF4051B	DIL, SO	16	8-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	15	-40 to +125	Vis to Vos	5.0	60
HEF4052B	DIL, SO	16	Dual 4-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	15	-40 to +125	Vis to Vos	5.0	60
HEF4053B	DIL, SO, TSSOP	16	Triple 2-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	15	-40 to +125	Vis to Vos	5.0	60
HEF4066B	DIL, SO	14	Quad Bilateral Switch; Low "ON" Resistance	Yes	CMOS	15	-40 to +125	Vis to Vos	5.0	60
HEF4067B	DIL, SO	24	16-Channel Analog Multiplexer/Demultiplexer	Yes	CMOS	15	-40 to +125	Vis to Vos	10.0	60
74LV4051	DIL, SO, SSOP, TSSOP	16	3.3V 8-Channel Analog Multiplexer/Demultiplexer	Yes	TTL	3.3	-40 to +125	Vis to Vos	5.0	55
74LV4052	DIL, SO, SSOP, TSSOP	16	3.3V Dual 4-Channel Analog Multiplexer/Demultiplexer	Yes	TTL	3.3	-40 to +125	Vis to Vos	5.0	55
74LV4053	DIL, SO, SSOP, TSSOP	16	3.3V Triple 2-Channel Analog Multiplexer/Demultiplexer	Yes	TTL	3.3	-40 to +125	Vis to Vos	5.0	70
74LV4066	DIL, SO, SSOP, TSSOP	14	3.3V Quad Bilateral Switch; Low "ON" Resistance	Yes	TTL	3.3	-40 to +125	Vis to Vos	2.0	23
74LVC1G66	PicoGate	5	3.3V Single Bilateral Switch	Yes	TTL	3.3	-40 to +125	Vis to Vos	0.3	6
74LVC1G384	PicoGate	5	3.3V Single Analog Switch	Yes	TTL	3.3	-40 to +125	Vis to Vos	0.3	6
74LVC2G66	TSSOP, VSSOP	8	3.3V Dual Bilateral Switch	Yes	TTL	3.3	-40 to +125	Z to Y	0.3	6
74LVCV2G66	TSSOP, VSSOP	8	3.3V Dual Overvoltage Tolerant Bilateral Switch	Yes	TTL	3.3	-40 to +125	Y to Z	0.3	6
74LVC4066	SO, TSSOP, DQFN	14	3.3V Quad Bilateral Switch	Yes	TTL	3.6	-40 to +125	Vis to Vos	0.3	6

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT125	DIL, SO, SSOP, TSSOP	14	5V Quad Buffer/Line Driver with Active LOW Output Enable (3-State)	No	TTL	5	-40 to +85	A to Y	2.9	-32	64
74ABT126	SO, SSOP, TSSOP	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable (3-State)	No	TTL	5	-40 to +85	A to Y	2.9	-32	64
74ABT240	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	3.5	-32	64
74ABT2240	DIL, SO, TSSOP	20	5V Buffer/Line Driver; Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	4.3	-32	12
74ABT241	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	2.7	-32	64
74ABT2241	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.9	-32	12
74ABT244	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	2.9	-32	64
74ABT2244	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.9	-32	12
74ABT540	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver; Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	3.1	-32	64
74ABT541	DIL, SO, SSOP, TSSOP	20	5V Octal Buffer/Line Driver (3-State)	No	TTL	5	-40 to +85	A to Y	2.9	-32	64
74ABT827	DIL, SO, SSOP, TSSOP	24	5V 10-Bit Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	3.0	-32	64
74ABT162240	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	2.7	-32	12
74ABT16240A	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	2.0	-32	64
74ABT162244	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.2	-32	12
74ABTH162244	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.2	-32	12
74ABT16244A	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	2.1	-32	64
74ABTH16244A	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	No	TTL	5	-40 to +85	A to Y	2.1	-32	64
74ABT16541	SSOP, TSSOP	48	5V 16-Bit Buffer/Line Driver (3-State)	No	TTL	5	-40 to +85	A to Y	2.0	-32	64
74AHC1G06	PicoGate	5	5V Single Inverter with Open-Drain Outputs	Yes	CMOS	5	-40 to +125	A to Y	4.3	OD	8
74AHCT1G06	PicoGate	5	5V Single Inverter with Open-Drain Outputs; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.5	OD	8
74AHC1G07	PicoGate	5	5V Single Buffer with Open-Drain Outputs	Yes	CMOS	5	-40 to +125	A to Y	6.0	OD	8
74AHCT1G07	PicoGate	5	5V Single Buffer with Open-Drain Outputs; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.5	OD	8
74AHC1G125	PicoGate	5	5V Single Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	3.4	-8	8
74AHCT1G125	PicoGate	5	5V Single Buffer/Line Driver with Active LOW Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	3.4	-8	8
74AHC1G126	PicoGate	5	5V Single Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	3.4	-8	8
74AHCT1G126	PicoGate	5	5V Single Buffer/Line Driver with Active HIGH Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	3.4	-8	8
74AHC2G125	TSSOP, VSSOP	8	5V Dual Bus Buffer/Line Driver (3-State)	Yes	CMOS	5	-40 to +125	A to Y	4.8	-8	8
74AHCT2G125	TSSOP, VSSOP	8	5V Dual Bus Buffer/Line Driver; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	4.8	-8	8
74AHC2G126	TSSOP, VSSOP	8	5V Dual Bus Buffer/Line Driver (3-State)	Yes	CMOS	5	-40 to +125	A to Y	4.8	-8	8
74AHCT2G126	TSSOP, VSSOP	8	5V Dual Bus Buffer/Line Driver; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	4.8	-8	8
74AHC2G241	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver (3-State)	Yes	CMOS	5	-40 to +125	A to Y	3.4	-8	8
74AHCT2G241	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	4.7	-8	8

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC125	SO, TSSOP	14	5V Quad Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	4.3	-8	8
74AHCT125	SO, TSSOP	14	5V Quad Buffer/Line Driver with Active LOW Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	4.3	-8	8
74AHC126	SO, TSSOP	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	4.7	-8	8
74AHCT126	SO, TSSOP	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	4.7	-8	8
74AHC244	SO, TSSOP	20	5V Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	5.0	-8	8
74AHCT244	SO, TSSOP	20	5V Buffer/Line Driver; Non-Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74AHC541	SO, TSSOP	20	5V Octal Buffer/Line Driver (3-State)	Yes	CMOS	5	-40 to +125	A to Y	5.0	-8	8
74AHCT541	SO, TSSOP	20	5V Octal Buffer/Line Driver; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74ALVC125	SO, TSSOP	14	3.3V Quad Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	1.8	-24	24
74ALVC244	SO, TSSOP, DQFN	20	3.3V Octal Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.2	-24	24
74ALVC541	SO, TSSOP	20	3.3V Octal Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.3	-24	24
74ALVC16244	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	1.9	-24	24
74ALVCH16244	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	1.10	-24	24
74ALVCH162244	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.7	-12	12
74ALVCH16540	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	1.8	-24	24
74ALVCH16825	TSSOP	56	3.3V 18-Bit Buffer/Line Driver with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.0	-24	24
74ALVCH16827	TSSOP	56	3.3V 20-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.0	-24	24
74ALVCH162827	TSSOP	56	3.3V 20-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.9	-12	12
74ALVT16240	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to Y	1.7	-32	64
74ALVT162240	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.6	-12	12
74ALVT16241	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to Y	1.3	-32	64
74ALVT162241	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.2	-12	12
74ALVT16244	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to Y	1.5	-32	64
74ALVT162244	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.2	-12	12
74ALVT16344	SSOP, TSSOP	56	3.3V 1-to-4 Address Driver with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.9	-32	64
74ALVT16827	SSOP, TSSOP	56	3.3V 20-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.3	-32	64
74ALVT162827	SSOP, TSSOP	56	3.3V 20-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to B	2.2	-12	12

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AVC16244	TSSOP	48	2.5V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	2.5	-40 to +85	A to Y	1.3	-12	12
74AVCH16244	TSSOP	48	2.5V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	CMOS	2.5	-40 to +85	A to Y	1.5	-12	12
74F06	DIL, SO	14	5V Hex Inverter with Open-Drain Outputs	No	TTL	5	0 to +70	A to Y	3.5	OC	64
74F07	DIL, SO	14	5V Hex Buffer with Open-Drain Outputs	No	TTL	5	0 to +70	A to Y	5.0	OC	64
74F38	DIL, SO, DIL, SO	14	5V 2-Input NAND Buffer (Open Collector)	No	TTL	5	0 to +70	D to Q	7.0	OC	64
74F125	DIL, SO	14	5V Quad Buffer/Line Driver with Active LOW Output Enable (3-State)	No	TTL	5	0 to +70	D to Q	4.5	-15	64
74F126	DIL, SO	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable (3-State)	No	TTL	5	0 to +70	D to Q	4.5	-15	64
74F240	DIL, SO, SSOP	20	5V Octal Buffer/Line Driver; Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.5	-15	64
74F241	DIL, SO	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.0	-15	64
74F244	DIL, SO, SSOP, DIL, SO	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.0	-15	64
74F244B	DIL, SO	20	5V Octal Buffer/Line Driver with Reduced Ground-Noise Effects; Non-Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.5	-15	64
74F367	DIL, SO	16	5V Hex Buffer/Line Driver (3-State)	No	TTL	5	0 to +70	I to Y	5.5	-15	64
74F540	DIL, SO	20	5V Octal Buffer/Line Driver; Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.5	-15	64
74F541	DIL, SO	20	5V Octal Buffer/Line Driver (3-State)	No	TTL	5	0 to +70	I to Y	6.0	-15	64
74F656A	DIL, SO, DIL, SO	24	5V Octal Buffer/Line Driver with Parity; Non-Inverting (3-State)	No	TTL	5	0 to +70	D to Q	5.5	-15	48
74F804	DIL, SO	20	5V 2-Input NAND Driver	No	TTL	5	0 to +70	D to Q	3.0	-48	48
74F827	DIL, SO, SSOP	24	5V 10-Bit Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	0 to +70	D to Q	5.5	-24	64
74F1244	DIL, SO	20	5V Buffer/Light Load F244 (3-State)	No	TTL	5	0 to +70	I to Y	5.0	-15	64
74F1804	DIL, SO	20	5V 2-Input NAND Driver Center Power Pin F804	No	TTL	5	0 to +70	D to Q	3.0	-48	48
74F3037	DIL, SO, DIL, SO	16	5V Quad 2-Input NAND 30 Ω Line Driver; Inverting	No	TTL	5	0 to +70	D to Q	2.0	-67	160
74F3038	DIL, SO	16	5V Quad 2-Input NAND 30 Ω Line Driver; Inverting (Open Collector)	No	TTL	5	0 to +70	D to Q	8.5	OC	160
74HC1G125	PicoGate	5	5V Single Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	8.0	-2.6	2.6
74HCT1G125	PicoGate	5	5V Single Buffer/Line Driver with Active LOW Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	11.0	-2	2
74HC1G126	PicoGate	5	5V Single Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-2.6	2.6
74HCT1G126	PicoGate	5	5V Single Buffer/Line Driver with Active HIGH Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	11.0	-2	2
74HC2G125	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	8.0	-7.8	7.8
74HCT2G125	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver with Active LOW Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	15.0	-6	6
74HC2G126	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	8.0	-7.8	7.8
74HCT2G126	TSSOP, VSSOP	8	5V Dual Buffer/Line Driver with Active HIGH Output Enable; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	15.0	-6	6
74HC3G06	TSSOP, VSSOP	8	5V Triple Open Drain Inverter	Yes	CMOS	5	-40 to +125	A to Y	8.0	OD	5.2
74HCT3G06	TSSOP, VSSOP	8	5V Triple Open Drain Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	9.0	OD	4
74HC3G07	TSSOP, VSSOP	8	5V Triple Open Drain Buffer	Yes	CMOS	5	-40 to +125	A to Y	7.0	OD	5.2
74HCT3G07	TSSOP, VSSOP	8	5V Triple Open Drain Buffer; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	OD	4
74HC3G34	TSSOP, VSSOP	8	5V Triple Buffer Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74HCT3G34	TSSOP,VSSOP	8	5V Triple Buffer Gate;TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	10.0	-4	4
74HC125	DIL, SO, SSOP,TSSOP	14	5V Quad Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT125	DIL, SO, SSOP,TSSOP	14	5V Quad Buffer/Line Driver with Active LOW Output Enable;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	15.0	-6	6
74HC126	DIL, SO, SSOP,TSSOP	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT126	DIL, SO, SSOP,TSSOP	14	5V Quad Buffer/Line Driver with Active HIGH Output Enable;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	14.0	-6	6
74HC240	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT240	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	11.0	-6	6
74HC241	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	7.0	-7.8	7.8
74HCT241	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	13.0	-6	6
74HC244	DIL, SO, SSOP,TSSOP, DQFN	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT244	DIL, SO, SSOP,TSSOP, DQFN	20	5V Octal Buffer/Line Driver; Non-Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	13.0	-6	6
74HC365	DIL, SO, SSOP,TSSOP	16	5V Hex Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT365	DIL, SO, SSOP	16	5V Hex Buffer/Line Driver; Non-Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	14.0	-6	6
74HC366	DIL, SO	16	5V Hex Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	10.0	-7.8	7.8
74HCT366	DIL, SO, SSOP	16	5V Hex Buffer/Line Driver; Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	13.0	-6	6
74HC367	DIL, SO, SSOP,TSSOP	16	5V Hex Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	8.0	-7.8	7.8
74HCT367	DIL, SO, SSOP,TSSOP	16	5V Hex Buffer/Line Driver; Non-Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	14.0	-6	6
74HC368	DIL, SO, SSOP	16	5V Hex Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT368	DIL, SO, SSOP,TSSOP	16	5V Hex Buffer/Line Driver; Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	13.0	-6	6
74HC540	DIL, SO, SSOP	20	5V Octal Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	9.0	-7.8	7.8
74HCT540	DIL, SO, SSOP	20	5V Octal Buffer/Line Driver; Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	13.0	-6	6
74HC541	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	10.0	-7.8	7.8
74HCT541	DIL, SO, SSOP,TSSOP	20	5V Octal Buffer/Line Driver; Non-Inverting;TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	15.0	-6	6
HEF4049B	DIL, SO	16	Buffer; Inverting	Yes	CMOS	15	-40 to +125	I to O	12.0	-3	20
HEF4050B	DIL, SO	16	Buffer; Non-Inverting	Yes	CMOS	15	-40 to +125	I to O	15.0	-3	20
HEF40098B	DIL, SO	16	Hex Inverting Buffer (3-State)	Yes	CMOS	15	-40 to +125	I to O	25.0	-10	20
HEF40240B	DIL, SO	20	Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	15	-40 to +125	I to O	30.0	-62	45
HEF40244B	DIL, SO	20	Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	15	-40 to +125	I to O	30.0	-62	45
74LV125	DIL, SO, SSOP,TSSOP	14	3.3V Quad Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-12	12
74LV241	DIL, SO, SSOP,TSSOP	20	3.3V Octal Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	9.0	-8	8
74LV244	DIL, SO, SSOP,TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	9.0	-16	16
74LV365	DIL, SO, SSOP,TSSOP	16	3.3V Hex Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-8	8
74LV367	DIL, SO, SSOP,TSSOP	16	3.3V Hex Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-8	8

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74LV541	DIL, SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	11.0	-8	8
74LVC1G06	PicoGate	5	3.3V Single Inverter With Open-Drain Outputs	Yes	TTL	3.3	-40 to +125	A to Y	2.3	OD	32
74LVC1G07	PicoGate	5	3.3V Single Buffer With Open-Drain Outputs	Yes	TTL	3.3	-40 to +125	A to Y	2.2	OD	32
74LVC1G125	PicoGate	5	3.3V Single Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC1G126	PicoGate	5	3.3V Single Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.0	-32	32
74LVC2G06	PicoGate	6	3.3V Dual Open Drain Inverter	Yes	TTL	3.3	-40 to +125	A to Y	2.3	OD	32
74LVC2G07	PicoGate	6	3.3V Dual Open Drain Buffer	Yes	TTL	3.3	-40 to +125	A to Y	2.6	OD	32
74LVC2G34	PicoGate	6	3.3V Dual Buffer	Yes	TTL	3.3	-40 to +125	A to Y	2.2	-32	32
74LVC2G38	TSSOP, VSSOP	8	3.3V Dual 2-Input NAND Buffer (Open-Drain)	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC2G125	TSSOP, VSSOP	8	3.3V Dual Bus Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.3	-32	32
74LVC2G126	TSSOP, VSSOP	8	3.3V Dual Bus Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.4	-32	32
74LVC2G240	TSSOP, VSSOP	8	3.3V Dual Buffer/Line Driver with 5V-Tolerant Inputs/Outputs; Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.5	-32	32
74LVC2G241	TSSOP, VSSOP	8	3.3V Dual Buffer/Line Driver with 5V-Tolerant Inputs/Outputs (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.6	-32	32
74LVC3G06	TSSOP, VSSOP	8	3.3V Triple Inverter with Open Drain	Yes	TTL	3.3	-40 to +125	A to Y	2.0	na	32
74LVC3G07	TSSOP, VSSOP	8	3.3V Triple Buffer with Open Drain	Yes	TTL	3.3	-40 to +125	A to Y	2.1	na	32
74LVC3G34	TSSOP, VSSOP	8	3.3V Triple Buffer Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.2	-32	32
74LVC06A	SO, TSSOP, DQFN	14	3.3V Hex Inverter with Open-Drain Outputs	Yes	TTL	3.3	-40 to +125	A to Y	2.3	OD	32
74LVC07A	SO, TSSOP, DQFN	14	3.3V Hex Buffer with Open-Drain Outputs	Yes	TTL	3.3	-40 to +125	A to Y	2.2	OD	32
74LVC38A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input NAND Buffer (Open Drain)	Yes	TTL	3.3	-40 to +125	A to Y	2.8	-24	24
74LVC125A	SO, SSOP, TSSOP, DQFN	14	3.3V Buffer/Line Driver with Active LOW Output Enable (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.4	-24	24
74LVC126A	SO, SSOP, TSSOP, DQFN	14	3.3V Buffer/Line Driver with Active HIGH Output Enable (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.4	-24	24
74LVC240A	SO, SSOP, TSSOP, DQFN	20	3.3V Buffer/Line Driver; Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	3.5	-24	24
74LVC241A	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	3.2	-24	24
74LVC244A	SO, SSOP, TSSOP, DQFN	20	3.3V Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.8	-24	24
74LVCH244A	SO, SSOP, TSSOP, DQFN	20	3.3V Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	2.8	-24	24
74LVC2244A	SO, SSOP, TSSOP, DQFN	20	3.3V Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	3.1	-12	12
74LVC541A	SO, SSOP, TSSOP, DQFN	20	3.3V Buffer/Line Driver (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	3.3	-24	24
74LVC827A	SO, SSOP, TSSOP	24	3.3V 10-Bit Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	A to Y	4.0	-24	24
74LVC16240A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.8	-24	24
74LVC16241A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.4	-24	24
74LVC16244A	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-24	24
74LVCH16244A	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-24	24
74LVC162244A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-12	12
74LVCH162244A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-12	12
74LVCH16541A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.3	-24	24
74LVCH32244A	LFBGA	96	3.3V 32-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-24	24
74LVCH322244A	LFBGA	96	3.3V 32-Bit Buffer/Line Driver; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	3.0	-12	12

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74LVT125	SO, SSOP, TSSOP, DQFN	14	3.3V Buffer/Line Driver with Active LOW Output Enable (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.9	-32	64
74LVT126	SO, SSOP, TSSOP, DQFN	14	3.3V Buffer/Line Driver with Active HIGH Output Enable (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.4	-32	64
74LVT240	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.5	-32	64
74LVT241	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.8	-32	64
74LVT2241	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	3.3	-12	12
74LVT2244	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.9	-12	12
74LVT244A	SO, SSOP, TSSOP	20	3.3V Octal Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.6	-32	64
74LVT244B	SO, SSOP, TSSOP	20	3.3V Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	1.3	-32	64
74LVT16240A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.0	-32	64
74LVT162240A	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to Y	2.6	-12	12
74LVT16244B	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	3.3	-40 to +85	A to Y	1.8	-32	64
74LVT162244B	SSOP, TSSOP	48	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with 30 Ω Termination Resistors	No	TTL	3.3	-40 to +85	A to Y	2.8	-12	12
MB2244	PQFP	52	5V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to Y	3.2	-32	64

Bus Arbiters

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74F786	DIL, SO	16	5V 4-Bit Asynchronous Bus Arbiter	No	TTL	5	0 to +70	A to Y	5.0	-1	24

Product	Packages	Pins	Description	Operating Voltage	Temperature Range °C	T _{PD} where	Diode Type	# of Bits	T _{PD} 5.0V	R _{ON}
CBT3125	SO, SSOP, TSSOP, QSOP	14,16	Quadruple FET Bus Switch	4.5 to 5.5V	-40 to +85	OE to A		4	5.4	5
CBT3126	SO, SSOP, TSSOP, QSOP	14,16	Quadruple FET Bus Switch	4.5 to 5.5V	-40 to +85	OE to A		4	4.5	5
CBT3244A	SO, SSOP, TSSOP, QSOP	20	Octal Bus Switch with Quad Output Enables	4.5 to 5.5V	-40 to +85	OE to A		8	5	5
CBT3245A	SO, SSOP, QSOP, TSSOP	20	Octal Bus Switch with Output Enable	4.5 to 5.5V	-40 to +85	OE to A		8	5.9	5
CBT3253	SO, SSOP, QSOP, TSSOP	16	Dual 1-of-4 FET Multiplexer/Demultiplexer	4.5 to 5.5V	-40 to +85	OE to A		2	7.0	5
CBTS3253	SO, SSOP, QSOP, TSSOP	16	Dual 1-of-4 FET Multiplexer/Demultiplexer with Schottky Diode	4.5 to 5.5V	-40 to +85	OE to A	Schottky Undershoot	2	7.0	5
CBT3257	SO, SSOP, QSOP, TSSOP	16	Quad 1-of-2 Multiplexer/Demultiplexer	4.5 to 5.5V	-40 to +85	OE to A		4	5.5	5
CBTS3257	SO, SSOP, QSOP, TSSOP	16	Quad 1-of-2 Multiplexer/Demultiplexer with Schottky Diode	4.5 to 5.5V	-40 to +85	OE to A	Schottky Undershoot	4	5.5	5
CBT3306	SO, TSSOP	8	Dual Bus Switch	4.5 to 5.5V	-40 to +85	OE to A		2	5.0	3.4
CBTD3306	SO, TSSOP	8	Dual Bus Switch with Level Shifting	4.5 to 5.5V	-40 to +85	OE to A		2	5.0	3.6
CBTS3306	SO, TSSOP	8	Dual Bus Switch with Schottky Diode Clamping	4.5 to 5.5V	-40 to +85	OE to A	Level Shift	2	5.0	3.4
CBT3384	SO, SSOP, QSOP, TSSOP	24	10-Bit Bus Switch with Dual Output Enables	4.5 to 5.5V	-40 to +85	OE to A		10	5.7	5
CBTD3384	SO, SSOP, QSOP, TSSOP	24	10-Bit Level Shifting Bus Switch with 5-Bit Output Enables	4.5 to 5.5V	-40 to +85	OE to A	Level Shift	10	4.9	5
CBTS3384	SO, SSOP, TSSOP	24	10-Bit Bus Switch with 5-Bit Output Enables and Schottky Undershoot Protection	4.5 to 5.5V	-40 to +85	OE to A	Schottky Undershoot	10	4.5	5
CBT16210	SSOP, TSSOP	48	20-Bit Bus Switch with 10-Bit Output Enables	4.5 to 5.5V	-40 to +85	OE to A		20	4.0	5
CBTD16210	SSOP, TSSOP	48	20-Bit Level-Shifting Bus Switch with 10-Bit Output Enables	4.5 to 5.5V	-40 to +85	OE to A	Level Shift	20	6.0	5
CBT16211	SSOP, TSSOP	56	24-Bit Bus Switch with 12-Bit Output Enables	4.5 to 5.5V	-40 to +85	OE to A		24	8.5	5
CBTD16211	SSOP, TSSOP	56	24-Bit Level-Shifting Bus Switch with 12-Bit Output Enables	4.5 to 5.5V	-40 to +85	OE to A	Level Shift	24	6.5	5
CBT16212	SSOP, TSSOP	56	24-Bit Bus Exchange Switch with 12-Bit Output Enables	4.5 to 5.5V	-40 to +85	SEL to A		24	6.0	4
CBTD16212	SSOP, TSSOP	56	24-Bit Level-Shifting Bus Exchange Switch with 12-Bit Output Enables	4.5 to 5.5V	-40 to +85	SEL to A	Level Shift	24	8.0	5
CBTD16213	SSOP, TSSOP	56	24-Bit Level-Shifting Bus Exchange Switch with 12-Bit Output Enables	4.5 to 5.5V	-40 to +85	SEL to A	Level Shift	24	8.0	4

Device Type	Supply voltage V	Data throughput rate Mb/s	Inputs	Outputs	Propagation delay ps	On-resistance ohms	Operating temperature °C	Other features	Packages	Intended application
CBT3857	3.3	200	10	10	750	24	0~+85	10k Ω pull-down resistors	TSSOP-24	DDR DIMM bus switch
CBTV4010	2.5	400	10	40	140	20	0~+85	100 Ω pull-down resistors	TFBGA-64	DDR bus switch
CBTV4011	2.5	400	10	40	100	10	0~+85	100 Ω pull-down resistors	TFBGA-64	DDR bus switch
CBTV4012	2.5	400	10	40	100	10	0~+85	400 Ω pull-down resistors	TFBGA-64	DDR bus switch
CBTV4020	2.5	400	20	40	140	20	0~+85	100 Ω pull-down resistors	TFBGA-72	DDR bus switch

PCI Bus Switches

Product	Packages	Pins	Description	Operating Temperature Range °C	Diode Type	# of Bits	Multiplex Rate	T _{PD} where	T _{PD} 5.0V	R _{ON}
CBT6800	TSSOP	24	10-Bit Bus Switch with Precharged Outputs for Live Insertion	-40 to +85		10		OE to A	7.5	5
CBT6810	TSSOP, QSOP	24	10-Bit Bus Switch with Precharged Outputs and Schottky Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	10		OE to A	5.5	5
CBT6820	TSSOP	48	20-Bit Bus Switch with Precharged Outputs and Schottky Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	20		OE to A	4.4	5
CBT6832	TSSOP	56	16-Bit 1-of-2 Multiplexer/Demultiplexer with Precharged Outputs and Schottky Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	16	1 of 2	SEL to A	6.5	5
CBT6832C	TSSOP	56	16-Bit 1-of-2 Multiplexer/Demultiplexer with Precharged Outputs and Charge Pump Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	16	1 of 2	SEL to A	6.0	5
CBT6832D	TSSOP	56	16-Bit Controlled Enable Rate 1-of-2 Multiplexer/Demultiplexer with Precharged Outputs and Charge Pump Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	16	1 of 2	SEL to A	25.0	5
CBT6832E	TSSOP	56	16-Bit Controlled Enable Rate 1-of-2 Multiplexer/Demultiplexer with Precharged Outputs and Schottky Undershoot Protection for Live Insertion	-40 to +85	Schottky Undershoot	16	1 of 2	SEL to A	25.0	5

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74F85	DIL, SO	16	5V 4-Bit Magnitude Comparator	No	TTL	5	0 to +70	B to Equal	9.5	-1	20
74F521	DIL, SO	20	5V 8-Bit Identity Comparator	No	TTL	5	0 to +70	A to Q	8.0	-1	20
74HC85	DIL, SO, SSOP, TSSOP	16	5V 4-Bit Magnitude Comparator	Yes	CMOS	5	-40 to +125	B to Equal	17.0	-5.2	5.2
74HCT85	DIL, SO, SSOP	16	5V 4-Bit Magnitude Comparator; TTL Enabled	Yes	TTL	5	-40 to +125	B to Equal	26.0	-4	4
74HC688	DIL, SO, SSOP, TSSOP	20	5V 8-Bit Magnitude Comparator	Yes	CMOS	5	-40 to +125	B to Equal	16.0	-5.2	5.2
74HCT688	DIL, SO, SSOP, TSSOP	20	5V 8-Bit Magnitude Comparator; TTL Enabled	Yes	TTL	5	-40 to +125	B to Equal	20.0	-4	4
HEF4585B	DIL, SO	16	4-Bit Magnitude Comparator	Yes	CMOS	15	-40 to +125	A to O	45.0	-1.3	4.6

BCD Counters

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74HC160	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous BCD Decade Counter; Asynchronous Reset	Yes	CMOS	5	-40 to +125	Cp to Q	18.0	-5.2	5.2	55
74HCT160	DIL, SO, TSSOP	16	5V Presettable Synchronous BCD Decade Counter; Asynchronous Reset; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	25.0	-4	4	28
HEF4518B	DIL, SO	16	Dual BCD Counter	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	40

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74F161A	DIL, SO	16	5V Presettable Synchronous 4-Bit Binary Counter; Asynchronous Reset	No	TTL	5	0 to +70	Cp to Q	6.5	-1	20	130
74F163A	DIL, SO	16	5V Presettable Synchronous 4-Bit Binary Counter; Synchronous Reset	No	TTL	5	0 to +70	Cp to Q	6.5	-1	20	130
74F191	DIL, SO	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter with Reset and Ripple Clock	No	TTL	5	0 to +70	Cp to Q	7.5	-1	20	125
74F193	DIL, SO	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter with Separate Up/Down Clocks	No	TTL	5	0 to +70	Cp to Q	5.5	-1	20	125
74F269	DIL, SO, SSOP	24	5V 8-Bit Bidirectional Binary Counter	No	TTL	5	0 to +70	Cp to Q	6.5	-1	20	115
74F393	DIL, SO	14	5V Dual 4-Bit Binary Ripple Counter	No	TTL	5	0 to +70	Cp to Q	7.0	-1	20	130
74F579	DIL, SO	20	5V 8-Bit Bidirectional Binary Counter; 20-Pin (3-State)	No	TTL	5	0 to +70	Cp to I/O	7.5	-3	24	115
74HC161	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Counter; Asynchronous Reset	Yes	CMOS	5	-40 to +125	Cp to Q	18.0	-5.2	5.2	40
74HCT161	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Counter; Asynchronous Reset; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	23.0	-4	4	41
74HC163	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Counter; Synchronous Reset	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-5.2	5.2	46
74HCT163	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Counter; Synchronous Reset; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	23.0	-4	4	45
74HC191	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter	Yes	CMOS	5	-40 to +125	Cp to Q	21.0	-5.2	5.2	33
74HCT191	DIL, SO	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	26.0	-4	4	33
74HC193	DIL, SO, SSOP, TSSOP	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter; Separate Up/Down Clocks	Yes	CMOS	5	-40 to +125	Cp to Q	18.0	-5.2	5.2	41
74HCT193	DIL, SO, SSOP	16	5V Presettable Synchronous 4-Bit Binary Up/Down Counter; Separate Up/Down Clocks; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	23.0	-4	4	43
74HC393	DIL, SO, SSOP, TSSOP	14	5V Dual 4-Bit Binary Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	12.0	-5.2	5.2	90
74HCT393	DIL, SO, SSOP, TSSOP	14	5V Dual 4-Bit Binary Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	15.0	-4	4	48
74HC4040	DIL, SO, SSOP, TSSOP	16	5V 12-Stage Binary Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	82
74HCT4040	DIL, SO, SSOP, TSSOP	16	5V 12-Stage Binary Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	19.0	-4	4	72
74HC4059	DIL, SO, SSOP	24	5V Programmable Divide-By-N Counter	Yes	CMOS	5	-40 to +125	Cp to Q	17.0	-5.2	5.2	36
74HCT4059	DIL, SO	24	5V Programmable Divide-By-N Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	24.0	-4	4	36
74HC4060	DIL, SO, SSOP, TSSOP	16	5V 14-Stage Ripple-Carry Binary Counter/Divider and Oscillator	Yes	CMOS	5	-40 to +125	R to Q	29.0	-5.2	5.2	
74HCT4060	DIL, SO, SSOP	16	5V 14-Stage Ripple-Carry Binary Counter/Divider and Oscillator; TTL Enabled	Yes	TTL	5	-40 to +125	R to Q	36.1	-4	4	
74HC4520	DIL, SO, SSOP, TSSOP	16	5V Dual 4-Bit Synchronous Binary Counter	Yes	CMOS	5	-40 to +125	Cp to Q	22.0	-5.2	5.2	58
74HCT4520	DIL, SO, SSOP	16	5V Dual 4-Bit Synchronous Binary Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	28.0	-4	4	58
74HC6323A	SO	8	5V Programmable Ripple Counter with Oscillator (3-State)	Yes	CMOS	5	-40 to +125	X1 to Out	19.0	-7.8	7.8	85
74HCT6323A	SO	8	5V Programmable Ripple Counter with Oscillator; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	X1 to Out	24.0	-6	6	85
74HC40103	DIL, SO, SSOP, TSSOP	16	5V 8-Bit Synchronous Binary Down Counter	Yes	CMOS	5	-40 to +125	Cp to TC	28.0	-5.2	5.2	29
74HCT40103	DIL, SO, SSOP, TSSOP	16	5V 8-Bit Synchronous Binary Down Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to TC	35.0	-4	4	28
HEF4040B	DIL, SO	16	12-Stage Binary Counter	Yes	CMOS	15	-40 to +125	Cp to O	35.0	-3	3	50
HEF4059B	DIL, SO	24	Programmable Divide-By-N Counter	Yes	CMOS	15	-40 to +125	Cp to O	35.0	-7	20	20
HEF4060B	DIL, SO	16	14-Stage Ripple-Carry Binary Counter/Divider and Oscillator	Yes	CMOS	15	-40 to +125	R to Q	50.0	-3	3	30
HEF4516B	DIL, SO	16	Binary Up/Down Counter	Yes	CMOS	15	-40 to +125	Cp to O	45.0	-3	3	18
HEF4520B	DIL, SO	16	Dual 4-Bit Synchronous Binary Counter	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	40
HEF4526B	DIL, SO	16	Programmable 4-Bit Binary Down Counter	Yes	CMOS	15	-40 to +125	Cp to O	50.0	-3	3	32
HEF4750V	DIL	28	Frequency Synthesizer	Yes	CMOS	10	-40 to +85	na	0	-9	0.5	30
HEF4751V	DIL	28	Universal Divider	Yes	CMOS	10	-40 to +85	I to O	45.0	-3	3	24
HEF40193B	DIL, SO	16	4-Bit Up/Down Binary Counter	Yes	CMOS	15	-40 to +125	Cp to O	65.0	-3	3	18
74LV393	DIL, SO, SSOP, TSSOP	14	3.3V Dual 4-Bit Binary Ripple Counter	Yes	TTL	3.3	-40 to +125	Cp to Q	14.0	-6	6	90

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74LV4060	DIL, SO, SSOP, TSSOP	16	3.3V 14-Stage Ripple-Carry Binary Counter/Divider and Oscillator	Yes	TTL	3.3	-40 to +125	RS to Q	33.0	-6	6	
74LVC161	SO, SSOP, TSSOP, DQFN	16	3.3V Presettable Synchronous 4-Bit Binary Counter; Asynchronous Reset	Yes	TTL	3.3	-40 to +125	Cp to Q	4.9	-24	24	200
74LVC163	SO, SSOP, TSSOP, DQFN	16	3.3V Presettable Synchronous 4-Bit Binary Counter; Synchronous Reset	Yes	TTL	3.3	-40 to +125	Cp to Q	4.9	-24	24	200
74LVC169	SO, SSOP, TSSOP, DQFN	16	3.3V Presettable Synchronous 4-Bit Up/Down Binary Counter	Yes	TTL	3.3	-40 to +125	Cp to Q	5.0	-24	24	200

Ripple Counters

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74HC93	DIL, SO, SSOP	14	5V 4-Bit Binary Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	12.0	-5.2	5.2	91
74HCT93	DIL, SO	14	5V 4-Bit Binary Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-4	4	70
74HC390	DIL, SO, SSOP, TSSOP	16	5V Dual Decade Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	60
74HCT390	DIL, SO, SSOP	16	5V Dual Decade Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	21.0	-4	4	55
74HC4017	DIL, SO, SSOP, TSSOP	16	5V Johnson Decade Counter with 10 Decoded Outputs	Yes	CMOS	5	-40 to +125	Cp to Q	18.0	-5.2	5.2	70
74HCT4017	DIL, SO	16	5V Johnson Decade Counter with 10 Decoded Outputs; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	25.0	-4	4	61
74HC4020	DIL, SO, SSOP, TSSOP	16	5V 14-Stage Binary Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	11.0	-5.2	5.2	82
74HCT4020	DIL, SO, SSOP, TSSOP	16	5V 14-Stage Binary Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-4	4	47
74HC4024	DIL, SO, SSOP, TSSOP	14	5V 7-Stage Binary Ripple Counter	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	82
74HCT4024	DIL, SO, TSSOP	14	5V 7-Stage Binary Ripple Counter; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	17.0	-4	4	64
HEF4017B	DIL, SO	16	Johnson Decade Counter with 10 Decoded Outputs	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	24
HEF4020B	DIL, SO	16	14-Stage Binary Counter	Yes	CMOS	15	-40 to +125	Cp to O	30.0	-3	3	25
HEF4024B	DIL, SO	14	7-Stage Binary Ripple Counter	Yes	CMOS	15	-40 to +125	Cp to O	25.0	-3	3	25
HEF4521B	DIL, SO	16	24-Stage Frequency Divider and Oscillator	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3	35

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC138	SO,TSSOP	16	5V 3-to-8 Line Decoder/Demultiplexer; Inverting	Yes	CMOS	5	-40 to +125	A to Y	6.3	-8	8
74AHCT138	SO,TSSOP	16	5V 3-to-8 Line Decoder/Demultiplexer; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	6.2	-8	8
74AHC139	SO,TSSOP	16	5V Dual 2-to-4 Line Decoder/Demultiplexer	Yes	CMOS	5	-40 to +125	A to Y	5.6	-8	8
74AHCT139	SO,TSSOP	16	5V Dual 2-to-4 Line Decoder/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	6.5	-8	8
74F138	DIL, SO, DIL, SO	16	5V 3-to-8 Line Decoder/Demultiplexer; Inverting	No	TTL	5	0 to +70	A to Q	6.1	-1	20
74F139	DIL, SO	16	5V Dual 2-to-4 Line Decoder/Demultiplexer	No	TTL	5	0 to +70	A to Q	6.1	-1	20
74HC42	DIL, SO	16	5V BCD to Decimal Decoder (1-of-10)	Yes	CMOS	5	-40 to +125	A to Y	14.0	-5.2	5.2
74HCT42	DIL, SO	16	5V BCD to Decimal Decoder (1-of-10); TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	20.0	-4	4
74HC137	DIL, SO, SSOP	16	5V 3-to-8 Line Decoder/Demultiplexer with Address Latches; Inverting	Yes	CMOS	5	-40 to +125	A to Y	17.0	-5.2	5.2
74HCT137	DIL, SO	16	5V 3-to-8 Line Decoder/Demultiplexer with Address Latches; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	22.0	-4	4
74HC138	DIL, SO, SSOP, TSSOP, DQFN	16	5V 3-to-8 Line Decoder/Demultiplexer; Inverting	Yes	CMOS	5	-40 to +125	A to Y	12.0	-5.2	5.2
74HCT138	DIL, SO, SSOP, TSSOP, DQFN	16	5V 3-to-8 Line Decoder/Demultiplexer; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	20.0	-4	4
74HC139	DIL, SO, SSOP, TSSOP	16	5V Dual 2-to-4 Line Decoder/Demultiplexer	Yes	CMOS	5	-40 to +125	A to Y	11.0	-5.2	5.2
74HCT139	DIL, SO, SSOP, TSSOP	16	5V Dual 2-to-4 Line Decoder/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	16.0	-4	4
74HC154	DIL, SO, SSOP, TSSOP, DQFN	24	5V 4-to-16 Line Decoder/Demultiplexer	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2
74HCT154	DIL, SO, SSOP, TSSOP, DQFN	24	5V 4-to-16 Line Decoder/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	16.0	-4	4
74HC237	DIL, SO, SSOP	16	5V 3-to-8 Decoder/Demultiplexer with Address Latches	Yes	CMOS	5	-40 to +125	A to Y	17.0	-5.2	5.2
74HCT237	DIL, SO	16	5V 3-to-8 Decoder/Demultiplexer with Address Latches; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	22.0	-4	4
74HC238	DIL, SO, SSOP, TSSOP	16	5V 3-to-8 Decoder/Demultiplexer	Yes	CMOS	5	-40 to +125	A to Y	14.0	-5.2	5.2
74HCT238	DIL, SO, SSOP, TSSOP	16	5V 3-to-8 Decoder/Demultiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	21.0	-4	4
74HC4511	DIL, SO	16	5V BCD to 7-Segment Latch/Decoder/Driver with Lamp Test Input	Yes	CMOS	5	-40 to +125	D to Q	22.0	-15	5.2
74HCT4511	DIL, SO	16	5V BCD to 7-Segment Latch/Decoder/Driver with Lamp Test Input; TTL Enabled	Yes	TTL	5	-40 to +125	D to Q	28.0	-10	4
74HC4514	DIL, SO, SSOP, TSSOP	24	5V 4-of-16 Decoder/Demultiplexer with Input Latches; Outputs LOW at Data Input HIGH	Yes	CMOS	5	-40 to +125	A to Q	22.0	-5.2	5.2
74HCT4514	DIL, SO, SSOP, TSSOP	24	5V 4-of-16 Decoder/Demultiplexer with Input Latches; Outputs LOW at Data Input HIGH; TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	30.0	-4	4
74HC4515	DIL, SO	24	5V 4-to-16 Decoder/Demultiplexer with Input Latches; Outputs HIGH at Data Input HIGH; Inverting	Yes	CMOS	5	-40 to +125	A to Q	23.0	-5.2	5.2
74HCT4515	DIL, SO	24	5V 4-to-16 Decoder/Demultiplexer with Input Latches; Outputs HIGH at Data Input HIGH; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	30.0	-4	4
HEF4028B	DIL, SO	16	1-of-10 Decoder	Yes	CMOS	15	-40 to +125	A to O	30.0	-3	3
HEF4511B	DIL, SO	16	BCD to 7-Segment Latch/Decoder/Driver with Lamp Test Input	Yes	CMOS	15	-40 to +125	D to O	40.0	-25	25
HEF4514B	DIL, SO	24	1-of-16 Decoder/Demultiplexer with Input Latches; Outputs LOW at Data Input HIGH	Yes	CMOS	15	-40 to +125	A to O	65.0	-3	3
HEF4515B	DIL, SO	24	1-of-16 Decoder/Demultiplexer with Input Latches; Outputs HIGH at Data Input HIGH; Inverting	Yes	CMOS	15	-40 to +125	A to O	65.0	-3	3
HEF4543B	DIL, SO	16	BCD to 7-Segment Latch/Decoder/Driver with Phase Input	Yes	CMOS	15	-40 to +125	D to O	55.0	-3	3
HEF4555B	DIL, SO	16	Dual 1-to-4 Line Decoder/Demultiplexer	Yes	CMOS	15	-40 to +125	A to O	40.0	-3	3
74LV138	DIL, SO, SSOP, TSSOP	16	3.3V 3-to-8 Line Decoder/Demultiplexer; Inverting	Yes	TTL	3.3	-40 to +125	A to Y	15.0	-12	12
74LV139	DIL, SO, SSOP, TSSOP	16	3.3V Dual 2-to-4 Line Decoder/Demultiplexer	Yes	TTL	3.3	-40 to +125	A to Y	13.0	-12	12
74LVC1G18	PicoGate	6	3.3V 1-of-2 Non-Inverting Demultiplexer with 3-State Deselected Output	Yes	TTL	3.3	-40 to +125	A to Y	3.0	-32	32
74LVC1G19	PicoGate	6	3.3V 1-of-2 Decoder/Demultiplexer	Yes	TTL	3.3	-40 to +125	A to Y	2.5	-32	32
74LVC138A	SO, SSOP, TSSOP, DQFN	16	3.3V 3-to-8 Line Decoder/Demultiplexer; Inverting	Yes	TTL	3.3	-40 to +125	A to Y	2.6	-24	24
74LVC139	SO, SSOP, TSSOP, DQFN	16	3.3V Dual 2-to-4 Line Decoder/Demultiplexer	Yes	TTL	3.3	-40 to +125	A to Y	2.9	-24	24

Encoders

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74F148	DIL, SO	16	5V 8-Input Priority Encoder	No	TTL	5	0 to +70	I to A	6.0	-1	20
74HC147	DIL, SO, SSOP	16	5V 10-to-4 Line Priority Encoder	Yes	CMOS	5	-40 to +125	A to Y	14.0	-5.2	5.2
74HCT147	DIL, SO	16	5V 10-to-4 Line Priority Encoder; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	20.0	-4	4

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74ABT74	DIL, SO, SSOP, TSSOP	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	No	TTL	5	-40 to +85	Cp to Q	3.0	-15	20	250
74ABT273A	DIL, SO, SSOP, TSSOP	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger	No	TTL	5	-40 to +85	CP to Q	3.4	-32	64	350
74ABT374A	DIL, SO, SSOP, TSSOP	20	5V D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	CP to Q	3.8	-32	64	300
74ABT377A	DIL, SO, SSOP, TSSOP	20	5V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger	No	TTL	5	-40 to +85	CP to Q	3.6	-32	64	250
74ABT534A	DIL, SO, SSOP, TSSOP	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting (3-State)	No	TTL	5	-40 to +85	CP to Q	3.6	-32	64	350
74ABT574A	DIL, SO, SSOP, TSSOP	20	5V Octal D-Type Flip-Flop	No	TTL	5	-40 to +85	CP to Q	3.4	-32	64	400
74ABT821	DIL, SO, SSOP, TSSOP	24	5V 10-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	CP to Q	4.6	-32	64	185
74ABT823	DIL, SO, SSOP, TSSOP	24	5V 9-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	CP to Q	4.4	-32	64	200
74ABT16273	SSOP, TSSOP	48	5V 16-Bit D-Type Flip-Flop with Reset; Positive-Edge Trigger	No	TTL	5	-40 to +85	Cp to Q	2.5	-32	64	240
74ABT16374B	SSOP, TSSOP, PQFP	48, 48, 52	5V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	Cp to Q	2.6	-32	64	260
74ABT16821A	SSOP, TSSOP	56	5V 20-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	Cp to Q	2.4	-32	64	250
74ABTH16821A	TSSOP	56	5V 20-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	5	-40 to +85	Cp to Q	2.4	-32	64	250
74ABT16823A	SSOP, TSSOP	56	5V 18-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	-40 to +85	Cp to Q	2.3	-32	64	190
74AHC1G79	PicoGate	5	5V Single D-Type Flip-Flop; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	5.1	-8	8	90
74AHC1G79	PicoGate	5	5V Single D-Type Flip-Flop; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	5.0	-8	8	90
74AHC74	SO, TSSOP	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	5.2	-8	8	115
74AHC74	SO, TSSOP	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	4.8	-8	8	140
74AHC273	SO, TSSOP, DQFN	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	6.0	-8	8	110
74AHC273	SO, TSSOP, DQFN	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	5.8	-8	8	75
74AHC374	SO, TSSOP	20	5V D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	5.7	-8	8	120
74AHC374	SO, TSSOP	20	5V D-Type Flip-Flop; Positive-Edge Trigger; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	Cp to Q	5.6	-8	8	130
74AHC377	SO, TSSOP	20	5V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	5.6	-8	8	120
74AHC377	SO, TSSOP	20	5V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	5.7	-8	8	130
74AHC574	SO, TSSOP	20	5V Octal D-Type Flip-Flop	Yes	CMOS	5	-40 to +125	Cp to Q	6.2	-8	8	115
74AHC574	SO, TSSOP	20	5V Octal D-Type Flip-Flop; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	6.3	-8	8	115
74ALVC74	SO, TSSOP, DQFN	14	3.3V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +85	Cp to Q	2.7	-24	24	425
74ALVC374	SO, TSSOP	20	3.3V Octal D-Type Flip-Flop, Positive-Edge Trigger (3-State)	Yes	TTL	3.3	-40 to +85	Cp to Q	2.5	-24	24	300
74ALVC574	SO, TSSOP	20	3.3V Octal D-Type Flip-Flop, Positive-Edge Trigger (3-State)	Yes	TTL	3.3	-40 to +85	Cp to Q	2.5	-24	24	300
74ALVCH16374	SSOP, TSSOP	48	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	Cp to Q	2.3	-24	24	350
74ALVCH16821	SSOP, TSSOP	56	3.3V 20-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	Cp to Q	2.5	-24	24	350
74ALVCH16823	SSOP, TSSOP	56	3.3V 18-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	Cp to Q	2.5	-24	24	350
74ALVT16374	SSOP, TSSOP	48	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	Cp to Q	2.3	-32	64	175
74ALVT16821	SSOP, TSSOP	56	3.3V 20-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	Cp to Q	1.8	-32	64	175
74ALVT162821	SSOP, TSSOP	56	3.3V 20-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	Cp to Q	3.2	-12	12	175
74ALVT16823	SSOP, TSSOP	56	3.3V 18-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	Cp to Q	1.9	-32	64	275

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74ALVT162823	SSOP,TSSOP	56	3.3V 18-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	Cp to Q	3.0	-12	12	
74AVC16374	TSSOP	48	2.5V 16-Bit D-Type Flip-Flop; Postive-Edge Trigger (3-State)	Yes	CMOS	2.5	-40 to +85	Cp to Q	1.5	-12	12	350
74F74	DIL, SO	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	No	TTL	5	0 to + 70	Cp to Q	6.2	-1	20	125
74F174	DIL, SO	16	5V Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger	No	TTL	5	0 to + 70	Cp to Q	6.0	-1	20	100
74F175A	DIL, SO	16	5V Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger	No	TTL	5	0 to + 70	Cp to Q	6.0	-1	20	160
74F273A	DIL, SO	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger	No	TTL	5	0 to + 70	Cp to Q	7.0	-1	20	170
74F374	DIL, SO, SSOP	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger (3-State)	No	TTL	5	0 to + 70	Cp to Q	5.0	-3	24	165
74F534	DIL, SO	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting (3-State)	No	TTL	5	0 to + 70	Cp to Q	4.5	-3	24	165
74F574	DIL, SO	20	5V Octal D-Type Flip-Flop; Postive-Edge Trigger; Broadside Pinout Configuration (3-State)	No	TTL	5	0 to + 70	Cp to Q	5.0	-3	24	180
74F821	DIL, SO	24	5V 10-Bit D-Type Flip-Flop; Positive-Edge Trigger; Non-Inverting (3-State)	No	TTL	5	0 to + 70	Cp to Q	6.5	-24	64	180
74F5074	DIL, SO	14	5V Synchronizing Dual D-Type Flip-Flop/Clock Driver	No	TTL	5	0 to + 70	Cp to Q	3.9	-15	20	120
74HC74	DIL, SO, SSOP,TSSOP, DQFN	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	69
74HCT74	DIL, SO, SSOP,TSSOP, DQFN	14	5V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-4	4	54
74HC173	DIL, SO, SSOP,TSSOP	16	5V Quad D-Type Flip-Flop; Postive-Edge Trigger; 3-State	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-7.8	7.8	80
74HCT173	DIL, SO, SSOP	16	5V Quad D-Type Flip-Flop; Postive-Edge Trigger; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	Cp to Q	20.0	-6	6	80
74HC174	DIL, SO, SSOP,TSSOP	16	5V Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-5.2	5.2	90
74HCT174	DIL, SO, SSOP,TSSOP	16	5V Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	21.0	-4	4	63
74HC175	DIL, SO, SSOP,TSSOP	16	5V Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-5.2	5.2	75
74HCT175	DIL, SO, SSOP,TSSOP	16	5V Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	19.0	-4	4	49
74HC273	DIL, SO, SSOP,TSSOP, DQFN	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	13.0	-5.2	5.2	103
74HCT273	DIL, SO, SSOP,TSSOP, DQFN	20	5V D-Type Flip-Flop with Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	16.0	-4	4	56
74HC374	DIL, SO, SSOP,TSSOP	20	5V D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-7.8	7.8	70
74HCT374	DIL, SO, SSOP,TSSOP	20	5V D-Type Flip-Flop; Positive-Edge Trigger (3-State); TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	16.0	-6	6	44
74HC377	DIL, SO, SSOP,TSSOP	20	5V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	13.0	-5.2	5.2	70
74HCT377	DIL, SO, SSOP,TSSOP	20	5V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	17.0	-4	4	48
74HC534	DIL, SO	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	12.0	-7.8	7.8	55
74HCT534	DIL, SO	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	Cp to Q	16.0	-6	6	36
74HC564	DIL, SO	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-7.8	7.8	115
74HCT564	DIL, SO	20	5V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	Cp to Q	19.0	-6	6	56
74HC574	DIL, SO, SSOP,TSSOP	20	5V Octal D-Type Flip-Flop	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-7.8	7.8	112
74HCT574	DIL, SO, SSOP,TSSOP	20	5V Octal D-Type Flip-Flop; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-6	6	69
74HCT7273	DIL, SO	20	5V Octal D-Type Flip-Flop with Reset; Positive Edge-Trigger; Open Drain Outputs; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	16.0	OD	4	56
HEF4013B	DIL, SO	14	Dual D-Type Flip-Flop	Yes	CMOS	15	-40 to +125	Cp to O	30.0	-3	3	28
HEF40174B	DIL, SO	16	Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	15	-40 to +125	Cp to O	20.0	-3	3	45

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
HEF40175B	DIL, SO	16	Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	CMOS	15	-40 to +125	C _p to O	25.0	-3	3	45
HEF40374B	DIL, SO	20	D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	CMOS	15	-40 to +125	C _p to O	40.0	-62	45	17
74LV74	DIL, SO, SSOP, TSSOP	14	3.3V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	13.0	-12	12	110
74LV174	DIL, SO, SSOP, TSSOP	16	3.3V Hex D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	19.0	-12	12	100
74LV175	DIL, SO, SSOP, TSSOP	16	3.3V Quad D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	19.0	-6	6	70
74LV273	DIL, SO, SSOP, TSSOP	20	3.3V D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	14.0	-12	12	100
74LV374	DIL, SO, SSOP, TSSOP	20	3.3V D-Type Flip-Flop; Positive-Edge Trigger; 3-State	Yes	TTL	3.3	-40 to +125	C _p to Q	17.0	-16	16	70
74LV377	DIL, SO, SSOP, TSSOP	20	3.3V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	15.0	-6	6	70
74LV574	DIL, SO, SSOP, TSSOP	20	3.3V Octal D-Type Flip-Flop	Yes	TTL	3.3	-40 to +125	C _p to Q	15.0	-16	16	
74LVC1G74	TSSOP, VSSOP	8	3.3V Single D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	3.5	-32	32	280
74LVC1G79	PicoGate	5	3.3V Single D-Type Flip-Flop; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	2.2	-32	32	500
74LVC1G80	PicoGate	5	3.3V Single D-Type Flip-Flop; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	2.4	-32	32	500
74LVC1G175	PicoGate	6	3.3V Single D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	3.1	-24	24	300
74LVC74A	SO, SSOP, TSSOP, DQFN	14	3.3V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	D to Q	2.5	-24	24	250
74LVC273	SO, SSOP, TSSOP, DQFN	20	3.3V D-Type Flip-Flop with Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	6.0	-24	24	230
74LVC374A	SO, SSOP, TSSOP, DQFN	20	3.3V D-Type Flip-Flop; Positive-Edge Trigger; 3-State	Yes	TTL	3.3	-40 to +125	C _p to Q	2.7	-24	24	80
74LVC377	SO, SSOP, TSSOP	20	3.3V Octal D-Type Flip-Flop with Data Enable; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	C _p to Q	6.0	-24	24	230
74LVC574A	SO, SSOP, TSSOP, DQFN	20	3.3V Octal D-Type Flip-Flop	Yes	TTL	3.3	-40 to +125	C _p to Q	3.2	-24	24	150
74LVC821A	SO, SSOP, TSSOP, DQFN	24	3.3V 10-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	TTL	3.3	-40 to +125	C _p to Q	5.4	-24	24	200
74LVC823A	SO, SSOP, TSSOP, DQFN	24	3.3V 9-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	TTL	3.3	-40 to +125	C _p to Q	5.1	-24	24	200
74LVC16374A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger (3-State)	Yes	TTL	3.3	-40 to +85	C _p to Q	3.4	-24	24	150
74LVCH16374A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger; with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	C _p to Q	3.4	-24	24	150
74LVCH162374A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	C _p to Q	3.4	-12	12	
74LVCH32374A	LFBGA	96	3.3V 32-Bit D-Type Flip-Flop; Positive-Edge Trigger; with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	C _p to Q	3.4	-24	24	150
74LVT74	SO, SSOP, TSSOP	14	3.3V Dual D-Type Flip-Flop with Set and Reset; Positive-Edge Trigger	No	TTL	3.3	-40 to +85	C _p to Q	3.6	-20	32	345
74LVT273	SO, SSOP, TSSOP	20	3.3V D-Type Flip-Flop with Reset; Positive-Edge Trigger with Bus Hold	No	TTL	3.3	-40 to +85	C _p to Q	3.5	-32	64	150
74LVT374	SO, SSOP, TSSOP	20	3.3V D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to Q	3.5	-32	64	250
74LVT534	SO, SSOP, TSSOP	20	3.3V Octal D-Type Flip-Flop; Positive-Edge Trigger; Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to Q	3.5	-32	64	150
74LVT574	SO, SSOP, TSSOP	20	3.3V Octal D-Type Flip-Flop with Bus Hold	No	TTL	3.3	-40 to +85	C _p to Q	4.3	-32	64	150
74LVT162374	SSOP, TSSOP	48	3.3V 16-Bit Edge-Triggered D-Type Flip-Flop with 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	C _p to Q	3.0	-12	12	150
74LVT16374A	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit D-Type Flip-Flop; Positive-Edge Trigger with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to Q	3.0	-32	64	150
74LVT32374	LFBGA	96	3.3V 32-Bit Edge-Triggered D-Type Flip-Flop (3-State)	No	TTL	3.3	-40 to +85	C _p to Q	3.0	-32	64	150

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74F109	DIL, SO	16	5V Dual J-K Flip-Flop with Set and Reset; Positive-Edge Trigger	No	TTL	5	0 to +70	Cp to Q	6.2	-1	20	125
74F112	DIL, SO	16	5V Dual J-K Flip-Flop with Set and Reset; Negative-Edge Trigger	No	TTL	5	0 to +70	Cp to Q	5.0	-1	20	100
74F113	DIL, SO	14	5V Dual J-K Negative Edge-Triggered Flip-Flop without Reset	No	TTL	5	0 to +70	Cp to Q	4.0	-1	20	100
74HC73	DIL, SO, SSOP, TSSOP	14	5V Dual J-K Flip-Flop with Reset; Negative-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-5.2	5.2	70
74HCT73	DIL, SO, SSOP	14	5V Dual J-K Flip-Flop with Reset; Negative-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-4	4	72
74HC107	DIL, SO, SSOP, TSSOP	14	5V Dual J-K Flip-Flop with Reset; Negative-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-5.2	5.2	70
74HCT107	DIL, SO	14	5V Dual J-K Flip-Flop with Reset; Negative-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	19.0	-4	4	66
74HC109	DIL, SO, SSOP	16	5V Dual J-K Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	68
74HCT109	DIL, SO, SSOP, TSSOP	16	5V Dual J-K Flip-Flop with Set and Reset; Positive-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	20.0	-4	4	55
74HC112	DIL, SO, SSOP, TSSOP	16	5V Dual J-K Flip-Flop with Set and Reset; Negative-Edge Trigger	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-5.2	5.2	60
74HCT112	DIL, SO, SSOP, TSSOP	16	5V Dual J-K Flip-Flop with Set and Reset; Negative-Edge Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	21.0	-4	4	64
HEF4027B	DIL, SO	16	Dual JK Flip-Flop	Yes	CMOS	15	-40 to +125	Cp to O	30.0	-3	3	25
74LVC109	SO, SSOP, TSSOP	16	3.3V Dual J-K Flip-Flop with Set and Reset; Positive-Edge Trigger	Yes	TTL	3.3	-40 to +125	Cp to Q	3.8	-24	24	225

Full Adders

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74F283	DIL, SO	16	5V 4-Bit Binary Full Adder with Fast Carry	No	TTL	5	0 to +70	A to Sum	7.0	-1	20
74HC283	DIL, SO, SSOP, TSSOP	16	5V 4-Bit Binary Full Adder with Fast Carry	Yes	CMOS	5	-40 to +125	A to sum	15.0	-5.2	5.2
74HCT283	DIL, SO, SSOP, TSSOP	16	5V 4-Bit Binary Full Adder with Fast Carry; TTL Enabled	Yes	TTL	5	-40 to +125	A to sum	29.0	-4	4

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT08	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input AND Gate	No	TTL	5	-40 to +85	A to Y	2.4	-15	20
74AHC1G08	PicoGate	5	5V Single 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT1G08	PicoGate	5	5V Single 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.1	-8	8
74AHC2G08	TSSOP, VSSOP	8	5V Dual 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT2G08	TSSOP, VSSOP	8	5V Dual 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.1	-8	8
74AHC08	SO, TSSOP	14	5V Quad 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.2	-8	8
74AHCT08	SO, TSSOP	14	5V Quad 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.4	-8	8
74ALVC08	SO, TSSOP, DQFN	14	3.3V Quad 2-Input AND Gate	Yes	TTL	3.3	-40 to +85	A to Y	2.0	-24	24
74F08	DIL, SO	14	5V Quad 2-Input AND Gate	No	TTL	5	0 to +70	D to Q	4.1	-1	20
74F11	DIL, SO	14	5V Triple 3-Input AND Gate	No	TTL	5	0 to +70	D to Q	4.2	-1	20
74HC1G08	PicoGate	5	5V Single 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-2.6	2.6
74HCT1G08	PicoGate	5	5V Single 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	-2	2
74HC2G08	TSSOP, VSSOP	8	5V Dual 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HCT2G08	TSSOP, VSSOP	8	5V Dual 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	14.0	-4	4
74HC08	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCT08	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	14.0	-4	4
74HC11	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2
74HCT11	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	16.0	-4	4
74HC21	DIL, SO, SSOP	14	5V Dual 4-Input AND Gate	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2
74HCT21	DIL, SO, SSOP	14	5V Dual 4-Input AND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	15.0	-4	4
HEF4073B	DIL, SO	14	Triple 3-Input AND Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4081B	DIL, SO	14	Quad 2-Input AND Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4082B	DIL, SO	14	Dual 4-Input AND Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
74LV08	DIL, SO, SSOP, TSSOP	14	3.3V Quad 2-Input AND Gate	Yes	TTL	3.3	-40 to +125	A to Y	9.0	-12	12
74LVC1G08	PicoGate	5	3.3V Single 2-Input AND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC2G08	TSSOP, VSSOP	8	3.3V Dual 2-Input AND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC08A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input AND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-24	24
74LVC11	SO, SSOP, TSSOP, DQFN	14	3.3V Triple 3-Input AND Gate	Yes	TTL	3.3	-40 to +125	A to Y	3.7	-24	24
74LVT08	SO, SSOP, TSSOP	14	3.3V Quad 2-Input AND Gate	No	TTL	3.3	-40 to +85	A to Y	3.4	-20	32

AND-OR Gates

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74F51	DIL, SO	14	5V Dual 2-Wide 2-Input, 2-Wide 3-Input AND-OR-Invert Gate	No	TTL	5	0 to +70	D to Q	3.0	-1	20
74HC58	DIL, SO, SSOP	14	5V Dual AND-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74HC7266	DIL, SO, SSOP	14	5V Quad 2-Input EXCLUSIVE-NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	11.0	-5.2	5.2
HEF4077B	DIL, SO	14	Quad EXCLUSIVE-NOR Gate	Yes	CMOS	15	-40 to +125	A to O	30.0	-3	3

Exclusive OR Gates

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC1G86	PicoGate	5	5V Single 2-Input EXCLUSIVE-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.9	-8	8
74AHCT1G86	PicoGate	5	5V Single 2-Input EXCLUSIVE-OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74AHC86	SO, TSSOP	14	5V Quad 2-Input EXCLUSIVE-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.8	-8	8
74AHCT86	SO, TSSOP	14	5V Quad 2-Input EXCLUSIVE-OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.9	-8	8
74F86	DIL, SO	14	5V Quad 2-Input EXCLUSIVE-OR Gate	No	TTL	5	0 to +70	D to Q	4.3	-1	20
74HC1G86	PicoGate	5	5V Single 2-Input EXCLUSIVE-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	9.0	-2.6	2.6
74HCT1G86	PicoGate	5	5V Single 2-Input EXCLUSIVE-OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	13.0	-2	2
74HC2G86	TSSOP, VSSOP	8	5V Dual 2-Input EXCLUSIVE-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	9.0	-5.2	5.2
74HCT2G86	TSSOP, VSSOP	8	5V Dual 2-Input EXCLUSIVE-OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	-4	4
74HC86	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input EXCLUSIVE-OR Gate	Yes	CMOS	5	-40 to +125	A to Y	11.0	-5.2	5.2
74HCT86	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input EXCLUSIVE-OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	17.0	-4	4
HEF4030B	DIL, SO	14	Quad EXCLUSIVE-OR Gate	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
HEF4070B	DIL, SO	14	Quad EXCLUSIVE-OR Gate	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
74LV86	DIL, SO, SSOP, TSSOP	14	3.3V Quad 2-Input EXCLUSIVE-OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	13.0	-12	12
74LVC1G86	PicoGate	5	3.3V Single 2-Input EXCLUSIVE-OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.3	-32	32
74LVC1G386	PicoGate	6	3.3V Single 3-Input EXCLUSIVE-OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	4.5	-32	32
74LVC2G86	TSSOP	8	3.3V Dual 2-Input EXCLUSIVE-OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.3	-32	32
74LVC86A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input EXCLUSIVE-OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	3.0	-24	24
74LVT86	SO, SSOP, TSSOP	14	3.3V Quad 2-Input EXCLUSIVE-OR Gate	No	TTL	3.3	-40 to +85	A to Y	3.5	-20	32

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT00	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NAND Gate	No	TTL	5	-40 to +85	A to Y	2.5	-15	20
74ABT10	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input NAND Gate	No	TTL	5	-40 to +85	A to Y	3.3	-15	20
74ABT20	DIL, SO, SSOP, TSSOP	14	5V Dual 4-Input NAND Gate	No	TTL	5	-40 to +85	A to Y	2.7	-15	20
74AHC1G00	PicoGate	5	5V Single 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.9	-8	8
74AHC1G00	PicoGate	5	5V Single 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74AHC2G00	TSSOP, VSSOP	8	5V Dual 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.9	-8	8
74AHC2G00	TSSOP, VSSOP	8	5V Dual 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74AHC00	SO, TSSOP	14	5V Quad 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.5	-8	8
74AHC00	SO, TSSOP	14	5V Quad 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.5	-8	8
74AHC30	SO, TSSOP	14	5V 8-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	4.9	-8	8
74AHC30	SO, TSSOP	14	5V 8-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.7	-8	8
74ALVC00	SO, TSSOP, DQFN	14	3.3V Quad 2-Input NAND Gate	Yes	TTL	3.3	-40 to +85	A to Y	2.1	-24	24
74F00	DIL, SO	14	5V Quad 2-Input NAND Gate	No	TTL	5	0 to +70	D to Q	3.4	-1	20
74F10	DIL, SO	14	5V Triple 3-Input NAND Gate	No	TTL	5	0 to +70	D to Q	3.5	-1	20
74F20	DIL, SO	14	5V Dual 4-Input NAND Gate	No	TTL	5	0 to +70	D to Q	3.5	-1	20
74F30	DIL, SO	14	5V 8-Input NAND Gate	No	TTL	5	0 to +70	D to Q	3.2	-1	20
74F133	DIL, SO	16	5V 13-Input NAND Gate	No	TTL	5	0 to +70	D to Q	4.5	-1	20
74HC1G00	PicoGate	5	5V Single 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-2.6	2.6
74HCT1G00	PicoGate	5	5V Single 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	-2	2
74HC2G00	TSSOP, VSSOP	8	5V Dual 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCT2G00	TSSOP, VSSOP	8	5V Dual 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12	-4	4
74HC00	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCT00	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	-4	4
74HC03	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NAND Gate with Level Shift Capability	Yes	CMOS	5	-40 to +125	A to Y	8.0	OD	5.2
74HCT03	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NAND Gate with Level Shift Capability; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	OD	4
74HC10	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	9.0	-5.2	5.2
74HCT10	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	14.0	-4	4
74HC20	DIL, SO, SSOP, TSSOP	14	5V Dual 4-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HCT20	DIL, SO, SSOP	14	5V Dual 4-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	16.0	-4	4
74HC30	DIL, SO, SSOP, TSSOP	14	5V 8-Input NAND Gate	Yes	CMOS	5	-40 to +125	A to Y	12.0	-5.2	5.2
74HCT30	DIL, SO, SSOP, TSSOP	14	5V 8-Input NAND Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	16.0	-4	4
HEF4011B	DIL, SO	14	Quad 2-Input NAND Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4011UB	DIL, SO	14	Quad 2-Input NAND Gate (Unbuffered)	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4023B	DIL, SO	14	Triple 3-Input NAND Gate	Yes	CMOS	15	-40 to +125	I to O	15.0	-3	3
HEF4068B	DIL, SO	14	8-Input NAND Gate	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
74LV00	DIL, SO, SSOP, TSSOP	14	3.3V Quad 2-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	9.0	-12	12
74LV03	SO	14	3.3V Quad 2-Input NAND Gate with Level Shift Capability	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-12	12
74LV20	SO	14	3.3V Dual 4-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-6	6
74LVC1G00	PicoGate	5	3.3V Single 2-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.2	-32	32
74LVC2G00	TSSOP, VSSOP	8	3.3V Dual 2-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.2	-32	32
74LVC00A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-24	24
74LVC10A	SO, SSOP, TSSOP, DQFN	14	3.3V Triple 3-Input NAND Gate	Yes	TTL	3.3	-40 to +125	A to Y	3.9	-24	24
74LVT00	SO, SSOP, TSSOP	14	3.3V Quad 2-Input NAND Gate	No	TTL	3.3	-40 to +85	A to Y	2.7	-20	32
74LVT10	SO, SSOP, TSSOP	14	3.3V Triple 3-Input NAND Gate	No	TTL	3.3	-40 to +85	A to Y	3.8	-20	32

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT02	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NOR Gate	No	TTL	5	-40 to +85	A to Y	2.4	-15	20
74AHC1G02	PicoGate	5	5V Single 2-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT1G02	PicoGate	5	5V Single 2-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.9	-8	8
74AHC02	SO, TSSOP	14	5V Quad 2-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	5.2	-8	8
74AHCT02	SO, TSSOP	14	5V Quad 2-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.1	-8	8
74ALVC02	SO, TSSOP, DQFN	14	3.3V Quad 2-Input NOR Gate	Yes	TTL	3.3	-40 to +85	A to Y	2.2	-24	24
74F02	DIL, SO	14	5V Quad 2-Input NOR Gate	No	TTL	5	0 to +70	D to Q	3.4	-1	20
74F27	DIL, SO	14	5V Triple 3-Input NOR Gate	No	TTL	5	0 to +70	D to Q	3.0	-1	20
74F260	DIL, SO	14	5V Dual 5-Input NOR Gate	No	TTL	5	0 to +70	D to Q	4.0	-1	20
74HC1G02	PicoGate	5	5V Single 2-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-2.6	2.6
74HCT1G02	PicoGate	5	5V Single 2-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	-2	2
74HC2G02	TSSOP, VSSOP	8	5V Dual 2-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HCT2G02	TSSOP, VSSOP	8	5V Dual 2-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	-4	4
74HC02	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCT02	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	-4	4
74HC27	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HCT27	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	-4	4
74HC4002	DIL, SO, SSOP, TSSOP	14	5V Dual 4-Input NOR Gate	Yes	CMOS	5	-40 to +125	A to Y	9.0	-5.2	5.2
74HCT4002	DIL, SO, SSOP	14	5V Dual 4-Input NOR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	13.0	-4	4
HEF4000B	DIL, SO	14	Dual 3-Input NOR Gate and Inverter	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
HEF4001B	DIL, SO	14	Quad 2-Input NOR Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4002B	DIL, SO	14	Dual 4-Input NOR Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4025B	DIL, SO	14	Triple 3-Input NOR Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
74LV02	SO	14	3.3V Quad 2-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	7.5	-12	12
74LV27	SO	14	3.3V Triple 3-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	10.0	-12	12
74LVC1G02	PicoGate	5	3.3V Single 2-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC2G02	TSSOP, VSSOP	8	3.3V Dual 2-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.4	-32	32
74LVC02A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-24	24
74LVC27	SO, SSOP, TSSOP, DQFN	14	3.3V Triple 3-Input NOR Gate	Yes	TTL	3.3	-40 to +125	A to Y	3.4	-24	24
74LVT02	SO, SSOP, TSSOP	14	3.3V Quad 2-Input NOR Gate	No	TTL	3.3	-40 to +85	A to Y	2.8	-20	32

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT32	DIL, SO, SSOP, TSSOP	14	5V Quad 2-Input OR Gate	No	TTL	5	-40 to +85	A to Y	2.3	-15	20
74AHC1G32	PicoGate	5	5V Single 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT1G32	PicoGate	5	5V Single 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.8	-8	8
74AHC2G32	TSSOP, VSSOP	8	5V Dual 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT2G32	TSSOP, VSSOP	8	5V Dual 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.8	-8	8
74AHC32	SO, TSSOP, DQFN	14	5V Quad 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	4.1	-8	8
74AHCT32	SO, TSSOP, DQFN	14	5V Quad 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.3	-8	8
74ALVC32	SO, TSSOP	14	3.3V Quad 2-Input OR Gate	Yes	TTL	3.3	-40 to +85	A to Y	2.0	-24	24
74F32	DIL, SO	14	5V Quad 2-Input OR Gate	No	TTL	5	0 to +70	D to Q	4.1	-1	20
74HC1G32	PicoGate	5	5V Single 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-2.6	2.6
74HCT1G32	PicoGate	5	5V Single 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	10.0	-2	2
74HC2G32	TSSOP, VSSOP	8	5V Dual 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCT2G32	TSSOP, VSSOP	8	5V Dual 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	13.0	-4	4
74HC32	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	6.0	-5.2	5.2
74HCT32	DIL, SO, SSOP, TSSOP, DQFN	14	5V Quad 2-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	11.0	-4	4
74HC4075	DIL, SO, SSOP	14	5V Triple 3-Input OR Gate	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HCT4075	DIL, SO, SSOP, TSSOP	14	5V Triple 3-Input OR Gate; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	12.0	-4	4
HEF4071B	DIL, SO	14	Quad 2-Input OR Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
HEF4072B	DIL, SO	14	Dual 4-Input OR Gate	Yes	CMOS	15	-40 to +125	I to O	25.0	-3	3
HEF4075B	DIL, SO	14	Triple 3-Input OR Gate	Yes	CMOS	15	-40 to +125	I to O	20.0	-3	3
74LV32	DIL, SO, SSOP, TSSOP	14	3.3V Quad 2-Input OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	8.0	-12	12
74LVC1G32	PicoGate	5	3.3V Single 2-Input OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-32	32
74LVC2G32	TSSOP, VSSOP	8	3.3V Dual 2-Input OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.2	-32	32
74LVC32A	SO, SSOP, TSSOP, DQFN	14	3.3V Quad 2-Input OR Gate	Yes	TTL	3.3	-40 to +125	A to Y	2.1	-24	24
74LVT32	SO, SSOP, TSSOP	14	3.3V Quad 2-Input OR Gate	No	TTL	3.3	-40 to +85	A to Y	3.2	-20	32

Multiple Gates

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74LVC1G57	PicoGate	6	3.3V single low-power configurable multiple function gate	Yes	TTL	3.3	-40 to +125	A to Y	3.8	-24	24
74LVC1G58	PicoGate	6	3.3V single low-power configurable multiple function gate	Yes	TTL	3.3	-40 to +125	A to Y	3.8	-24	24

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT04	DIL, SO, SSOP, TSSOP	14	5V Hex Inverter	No	TTL	5	-40 to +85	A to Y	2.2	-15	20
74AHC1G04	PicoGate	5	5V Single Inverter	Yes	CMOS	5	-40 to +125	A to Y	4.5	-8	8
74AHCT1G04	PicoGate	5	5V Single Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.9	-8	8
74AHC1GU04	PicoGate	5	5V Single Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	3.6	-8	8
74AHC3G04	TSSOP, VSSOP	8	5V Triple Inverter	Yes	CMOS	5	-40 to +125	A to Y	4.5	-8	8
74AHCT3G04	TSSOP, VSSOP	8	5V Triple Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	4.9	-8	8
74AHC3GU04	TSSOP, VSSOP	8	5V Triple Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	3.5	-8	8
74AHC04	SO, TSSOP	14	5V Hex Inverter	Yes	CMOS	5	-40 to +125	A to Y	4.5	-8	8
74AHC04	SO, TSSOP	14	5V Hex Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	4.5	-8	8
74AHCT04	SO, TSSOP	14	5V Hex Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	3.5	-8	8
74ALVC04	SO, TSSOP, DQFN	14	3.3V Hex Inverter	Yes	TTL	3.3	-40 to +85	A to Y	2.0	-24	24
74F04	DIL, SO	14	5V Hex Inverter	No	TTL	5	0 to +70	A to Y	3.5	-1	20
74HC1G04	PicoGate	5	5V Single Inverter	Yes	CMOS	5	-40 to +125	A to Y	8.0	-2.6	2.6
74HCT1G04	PicoGate	5	5V Single Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	10.0	-2	2
74HC1GU04	PicoGate	5	5V Single Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	6.0	-2.6	2.6
74HC3G04	TSSOP, VSSOP	8	5V Triple Inverter	Yes	CMOS	5	-40 to +125	A to Y	6.0	-5.2	5.2
74HCT3G04	TSSOP, VSSOP	8	5V Triple Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	10.0	-4	4
74HC3GU04	TSSOP, VSSOP	8	5V Triple Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	5.0	-5.2	5.2
74HC04	DIL, SO, SSOP, TSSOP, DQFN	14	5V Hex Inverter	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
74HCU04	DIL, SO, SSOP, TSSOP	14	5V Hex Inverter (Unbuffered)	Yes	CMOS	5	-40 to +125	A to Y	6.0	-5.2	5.2
74HCT04	DIL, SO, SSOP, TSSOP, DQFN	14	5V Hex Inverter; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	10.0	-4	4
HEF4007UB	DIL, SO	14	Dual Complementary Pair and Inverter	Yes	CMOS	15	-40 to +125	G to D	15.0	-3	3
HEF4069UB	DIL, SO	14	Hex Inverter (Unbuffered)	Yes	CMOS	15	-40 to +125	I to O	15.0	-3	3
74LV04	DIL, SO, SSOP, TSSOP	14	3.3V Hex Inverter	Yes	TTL	3.3	-40 to +125	A to Y	8.0	-12	12
74LVU04	DIL, SO, SSOP, TSSOP	14	3.3V Hex Inverter (Unbuffered)	Yes	TTL	3.3	-40 to +125	A to Y	7.0	-12	12
74LVC1G04	PicoGate	5	3.3V Single Inverter	Yes	TTL	3.3	-40 to +125	A to Y	2.0	-32	32
74LVC1GU04	PicoGate	5	3.3V Single Inverter (Unbuffered)	Yes	TTL	3.3	-40 to +125	A to Y	1.6	-32	32
74LVC1GX04	PicoGate	6	3.3V Crystal Driver	Yes	TTL	3.3	-40 to +125	X to Y	2.8	-32	32
74LVC2G04	PicoGate	6	3.3V Dual Inverter	Yes	TTL	3.3	-40 to +125	A to Y	2.7	-32	32
74LVC2GU04	PicoGate	6	3.3V Dual Inverter (Unbuffered)	Yes	TTL	3.3	-40 to +125	A to Y	2.3	-32	32
74LVC3G04	TSSOP, VSSOP	8	3.3V Triple Inverter	Yes	TTL	3.3	-40 to +125	A to Y	2.7	-32	32
74LVC3GU04	TSSOP, VSSOP	8	3.3V Triple Inverter (Unbuffered)	Yes	TTL	3.3	-40 to +125	A to Y	2.3	-32	32
74LVC04A	SO, SSOP, TSSOP, DQFN	14	3.3V Hex Inverter	Yes	TTL	3.3	-40 to +125	A to Y	1.9	-24	24
74LVC04A	SO, SSOP, TSSOP, DQFN	14	3.3V Hex Inverter (Unbuffered)	Yes	TTL	3.3	-40 to +125	A to Y	2.0	-24	24
74LVT04	SO, SSOP, TSSOP	14	3.3V Hex Inverter	No	TTL	3.3	-40 to +85	A to Y	2.6	-20	32

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT373A	DIL, SO, SSOP, TSSOP	20	5V Octal D-Type Transparent Latch (3-State)	No	TTL	5	-40 to +85	D to Q	3.6	-32	64
74ABT573A	DIL, SO, SSOP, TSSOP	20	5V Octal D-Type Transparent Latch (3-State)	No	TTL	5	-40 to +85	D to Q	3.3	-32	64
74ABT841	DIL, SO, SSOP, TSSOP	24	5V 10-Bit Bus Interface Latch; Non-Inverting (3-State)	No	TTL	5	-40 to +85	D to Q	4.1	-32	64
74ABT843	DIL, SO, SSOP, TSSOP	24	5V 9-Bit Bus Interface Latch; Non-Inverting (3-State)	No	TTL	5	-40 to +85	D to Q	4.0	-32	64
74ABT16260	SSOP, TSSOP	56	5V 12-Bit to 24-Bit Multiplexed D-Type Latch (3-State)	No	TTL	5	-40 to +85	A to B	2.8	-32	64
74ABT16373B	SSOP, TSSOP	48	5V 16-Bit D-Type Transparent Latch (3-State)	No	TTL	5	-40 to +85	D to Q	2.5	-32	64
74ABT16841A	SSOP, TSSOP	56	5V 20-Bit Bus Interface Latch; Non-Inverting (3-State)	No	TTL	5	-40 to +85	D to Q	3.1	-32	64
74AHC259	SO, TSSOP	16	5V 8-Bit Addressable Latch	Yes	CMOS	5	-40 to +125	D to Q	5.3	-8	8
74AHCT259	SO, TSSOP	16	5V 8-Bit Addressable Latch; TTL Enabled	Yes	TTL	5	-40 to +125	D to Q	5.4	-8	8
74AHC373	SO, TSSOP	20	5V Octal D-Type Transparent Latch (3-State)	Yes	CMOS	5	-40 to +125	D to Q	5.3	-8	8
74AHCT373	SO, TSSOP	20	5V Octal D-Type Transparent Latch; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	D to Q	5.2	-8	8
74AHC573	SO, TSSOP	20	5V Octal D-Type Transparent Latch (3-State)	Yes	CMOS	5	-40 to +125	D to Q	5.5	-8	8
74AHCT573	SO, TSSOP	20	5V Octal D-Type Transparent Latch; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	D to Q	4.9	-8	8
74ALVC373	SO, TSSOP	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	2.2	-24	24
74ALVC573	SO, TSSOP, DQFN	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	2.2	-24	24
74ALVCH16373	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Y	2.1	-24	24
74ALVCH16841	TSSOP	56	3.3V 20-Bit Bus Interface Latch; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	2.4	-24	24
74ALVCH16843	TSSOP	56	3.3V 18-Bit Bus Interface Latch; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	2.1	-24	24
74ALVT16260	SSOP, TSSOP	56	3.3V 12-Bit to 24-Bit Multiplexed D-Type Latch with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	2.8	-32	64
74ALVT16373	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	D to Q	1.8	-32	64
74AVC16373	TSSOP	48	2.5V 16-Bit D-Type Transparent Latch (3-State)	Yes	CMOS	2.5	-40 to +85	D to Q	1.6	-12	12
74F259	DIL, SO	16	5V 8-Bit Addressable Latch	No	TTL	5	0 to +70	D to Q	7.0	-1	20
74F373	DIL, SO, SSOP	20	5V Octal D-Type Transparent Latch (3-State)	No	TTL	5	0 to +70	D to Q	5.3	-3	24
74F2373	DIL, SO	20	5V Octal D-Type Transparent Latch with 30 Ω Termination Resistors (3-State)	No	TTL	5	0 to +70	D to Q	5.3	-3	5
74F573	DIL, SO, SSOP	20	5V Octal D-Type Transparent Latch; Broadside Pinout Configuration (3-State)	No	TTL	5	0 to +70	D to Q	5.5	-3	24
74F841	DIL, SO	24	5V 10-Bit Bus Interface Latch; Non-Inverting (3-State)	No	TTL	5	0 to +70	D to Q	4.5	-24	48
74F842	DIL, SO	24	5V 10-Bit Bus Interface Latch; Inverting (3-State)	No	TTL	5	0 to +70	D to Q	5.5	-24	48
74HC75	DIL, SO, SSOP, TSSOP	16	5V Quad Bistable Transparent Latch	Yes	CMOS	5	-40 - +125	D to Q	10.0	-5.2	5.2
74HCT75	DIL, SO, SSOP	16	5V Quad Bistable Transparent Latch; TTL Enabled	Yes	TTL	5	-40 - +125	D to Q	15.0	-4	4
74HC259	DIL, SO, SSOP, TSSOP	16	5V 8-Bit Addressable Latch	Yes	CMOS	5	-40 - +125	D to Q	17.0	-5.2	5.2
74HCT259	DIL, SO, SSOP, TSSOP	16	5V 8-Bit Addressable Latch; TTL Enabled	Yes	TTL	5	-40 - +125	D to Q	23.0	-4	4
74HC373	DIL, SO, SSOP, TSSOP, DQFN	20	5V Octal D-Type Transparent Latch (3-State)	Yes	CMOS	5	-40 - +125	D to Q	12.0	-7.8	7.8
74HCT373	DIL, SO, SSOP, TSSOP, DQFN	20	5V Octal D-Type Transparent Latch; TTL Enabled (3-State)	Yes	TTL	5	-40 - +125	D to Q	17.0	-6	6
74HC563	DIL, SO	20	5V Octal D-Type Transparent Latch; Inverting (3-State)	Yes	CMOS	5	-40 - +125	D to Q	14.0	-7.8	7.8
74HCT563	DIL, SO, SSOP	20	5V Octal D-Type Transparent Latch; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 - +125	D to Q	18.0	-6	6
74HC573	DIL, SO, SSOP, TSSOP, DQFN	20	5V Octal D-Type Transparent Latch (3-State)	Yes	CMOS	5	-40 - +125	D to Q	14.0	-7.8	7.8
74HCT573	DIL, SO, SSOP, TSSOP, DQFN	20	5V Octal D-Type Transparent Latch; TTL Enabled (3-State)	Yes	TTL	5	-40 - +125	D to Q	20.0	-6	6
HEF4043B	DIL, SO	16	Quad R/S Latch with Active HIGH Set and Reset Inputs (3-State)	Yes	CMOS	15	-40 - +125	R to Q	25.0	-3	3
HEF4044B	DIL, SO	16	Quad R/S Latch with Active LOW Set and Reset Inputs (3-State)	Yes	CMOS	15	-40 - +125	R to Q	30.0	-3	3
HEF40373B	DIL, SO	20	Octal D-Type Transparent Latch (3-State)	Yes	CMOS	15	-40 - +125	E to O	40.0	-62	45
74LV259	DIL, SO, SSOP, TSSOP	16	3.3V 8-Bit Addressable Latch	Yes	TTL	3.3	-40 - +125	D to Q	20.0	-6	6
74LV373	DIL, SO, SSOP, TSSOP	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 - +125	D to Q	13.0	-16	16

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74LV573	DIL, SO, SSOP, TSSOP	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 - +125	D to Q	14.0	-16	16
74LVC373A	SO, SSOP, TSSOP, DQFN	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 - +125	D to Q	3.0	-24	24
74LVC573A	SO, SSOP, TSSOP, DQFN	20	3.3V Octal D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 - +125	D to Q	3.4	-24	24
74LVC841A	SO, SSOP, TSSOP, DQFN	24	3.3V 10-Bit Bus Interface Latch; Non-Inverting (3-State)	Yes	TTL	3.3	-40 - +125	D to Q	4.5	-24	24
74LVC16373A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.0	-24	24
74LVCH16373A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.0	-24	24
74LVC162373A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.3	-12	12
74LVCH162373A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.3	-12	12
74LVCH32373A	LFBGA	96	3.3V 32-Bit D-Type Transparent Latch with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.0	-24	24
74LVT573	SO, SSOP, TSSOP	20	3.3V Octal D-Type Transparent Latch (3-State)	No	TTL	3.3	-40 to +85	D to Q	2.7	-32	64
74LVT162373	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch with 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	D to Q	2.5	-12	12
74LVT16373A	SSOP, TSSOP	48	3.3V 16-Bit D-Type Transparent Latch (3-State)	No	TTL	3.3	-40 to +85	D to Q	1.9	-32	64

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ALVC164245	SSOP,TSSOP	48	3.3V 16-Bit Dual Supply Translating Transceiver (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.7	-24	24
74HC4049	DIL, SO, SSOP	16	5V Hex Inverting High-to-Low Level Shifter	Yes	CMOS	5	-40 to +125	A to Y	8.0	-5.2	5.2
74HC4050	DIL, SO, SSOP,TSSOP	16	5V Hex High-to-Low Level Shifter	Yes	CMOS	5	-40 to +125	A to Y	7.0	-5.2	5.2
HEF4104B	DIL, SO	16	Quad Low-to-High Voltage Translator with 3-State Outputs	Yes	CMOS	15	-40 to +125	I to O	70.0	-3	3
74LVC4245A	SO, SSOP,TSSOP	24	3.3V Octal Dual Supply Translating Transceiver (3-State)	Yes	TTL	3.3	-40 to +125	B to A	4.0	-24	24

GTL Logic Translators

Device	Packages	Pins	Description	V _{CC} Opt	Operating Temperature °C	T _{PD} where	T _{PD} 3.3V max (ns)	Drive I _{OH}	Drive I _{OL}	f _{MAX} MHz	Number of Bits	TTL Drive	GTL Drive	Voltage Translation Range V	T _{PD} 5.0V	Drive I _{OH}	Drive I _{OL}	f _{MAX}
GTL2000	SSOP TSSOP	48	22-Bit GTL Processor Voltage Clamp	na	-40 to +85	S to D	5.5	na	na	3.5	22	na	na	1.14 to 1.65 - 3.0 to 5.5	1.5	na	na	3.5
GTL2002	SO TSSOP	8	2-Bit GTL Processor Voltage Clamp	na	-40 to +85	S to D	5.5	na	na	3.5	2	na	na	1.14 to 1.65 - 3.0 to 5.5	1.5	na	na	3.5
GTL2005	TSSOP	14	Quad GTL/GTL+ to LVTTTL/TTL Bidirectional Non-Latched Translator	3.3	-40 to +85	A to B	2.5	OC	40	100	4	+/- 12	40	1.14 to 1.65 - 3.0 to 5.5	4.4	-12	12	
GTL2006	TSSOP	28	13-bit GTL/GTL+ to LVTTTL Translator	3.3	-40 to +85	A to B	10	OC	15	100	13	+/- 16	15	1.14 to 1.65 - 3.0 to 5.5	5.5	OC	15	
GTL2010	TSSOP	28	13-bit GTL/GTL+ to LVTTTL Translator with Power Good	3.3	-40 to +85	A to B	10	OC	15	100	13	+/- 16	15	1.14 to 1.65 - 3.0 to 5.5	1.5	na	na	3.5
GTL1655	TSSOP HVQFN	24	10-Bit GTL Processor Voltage Clamp	na	-40 to +85	S to D	5.5	na	na	3.5	10	na	na	1.14 to 1.65 - 3.0 to 5.5	5.3	OC	100	
GTL16612	TSSOP	64	16-Bit LVTTTL-to-GTL/GTL+ Bus Transceiver with Live Insertion	3.3	-40 to +85	A to B	6.2	OC	100	100	16	+/- 24	100	1.14 to 1.65 - 3.0 to 5.5	1.9	OC	40	

HSTL Logic Translators

Device Type	Supply voltage V	Inputs number x type	Outputs number x type	Propagation delay ns	Set-up time DATA-CLK ns	Hold time CLK-DATA ns	Operating temperature °C	Other features	Packages	Intended application
HSTL16918	3.3	9 x HSTL	18 x LVTTTL	2.4	2	1	0~+70		TSSOP-48	memory address latch
HSTL16919	3.3	9 x HSTL	18 x LVTTTL	2.4	2	1	0~+70	12kohm pull-up resistor on inputs	TSSOP-48	memory address latch

PTN Logic Translators

Device Type	Supply voltage V	f _{MAX} MHz	Data transfer rate Mb/s	Outputs number x type	Outputs number x type	Rise/fall time ps	Operating temperature °C	Other features	Packages	Intended application
PTN3310	3.0~3.6	750	1500	1 x LVDS	1 x Differential PECL	200	-40~+85		SO-8 TSSOP-8	high-speed serial logic translation
PTN3311	3.0~3.6	400	800	1 x Differential PECL	1 x LVDS	500	-40~+85		SO-8 TSSOP-8	high-speed serial logic translation
PTN3331	3.0~3.6	200	400	4 x LVTTTL	4 x LVDS	500	-40~+85	output enable	SO-16 TSSOP-16	high-speed serial logic translation
PTN3332	3.0~3.6	200	400	4 x LVDS	4 x LVTTTL	500	-40~+85	output enable	SO-16 TSSOP-16	high-speed serial logic translation
PTN3341	3.0~3.6	200	400	4 x LVTTTL	4 x LVDS	500	-40~+85	output enable; 50 Ω drive	SO-16 TSSOP-16	high-speed serial logic translation
PTN3342	3.0~3.6	200	400	4 x LVDS	4 x LVTTTL	500	-40~+85	output enable, 100 Ω termination resistor	SO-16 TSSOP-16	high-speed serial logic translation

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC157	SO,TSSOP	16	5V Quad 2-Input Multiplexer	Yes	CMOS	5	-40 to +125	I to Y	4.6	-8	8
74AHCT157	SO,TSSOP	14	5V Quad 2-Input Multiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	I to Y	4.7	-8	8
74AHC257	SO,TSSOP	16	5V Quad 2-Input Multiplexer (3-State)	Yes	CMOS	5	-40 to +125	I to Y	4.2	-8	8
74AHCT257	SO,TSSOP	16	5V Quad 2-Input Multiplexer; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	I to Y	4.9	-8	8
74F153	DIL, SO	16	5V Dual 4-Input Multiplexer	No	TTL	5	0 to +70	I to Y	5.0	-1	20
74F157A	DIL, SO	16	5V Quad 2-Input Multiplexer; Non-Inverting	No	TTL	5	0 to +70	I to Y	4.6	-1	20
74F158A	DIL, SO	16	5V Quad 2-Input Multiplexer; Inverting	No	TTL	5	0 to +70	I to Y	3.7	-1	20
74F253	DIL, SO	16	5V Dual 4-Input Multiplexer (3-State)	No	TTL	5	0 to +70	I to Y	5.0	-3	24
74F257A	DIL, SO	16	5V Quad 2-Input Multiplexer (3-State)	No	TTL	5	0 to +70	I to Y	4.5	-3	24
74F258A	DIL, SO	16	5V Quad 2-Input Multiplexer; Inverting (3-State)	No	TTL	5	0 to +70	I to Y	4.5	-3	24
74F298	DIL, SO	16	5V Quad 2-Input Multiplexer with Storage	No	TTL	5	0 to +70	Cp to Q	6.5	-1	20
74HC151	DIL, SO, SSOP,TSSOP	16	5V 8-input multiplexer	Yes	CMOS	5	-40 to +125	I to Y	19.0	-5.2	5.2
74HCT151	DIL, SO, SSOP,TSSOP	16	5V 8-input multiplexer; TTL enabled	Yes	TTL	5	-40 to +125	I to Y	22.0	-4	4
74HC153	DIL, SO, SSOP,TSSOP	16	5V Dual 4-Input Multiplexer	Yes	CMOS	5	-40 to +125	I to Y	14.0	-5.2	5.2
74HCT153	DIL, SO, SSOP,TSSOP	16	5V Dual 4-Input Multiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	I to Y	19.0	-4	4
74HC157	DIL, SO, SSOP,TSSOP	16	5V Quad 2-Input Multiplexer	Yes	CMOS	5	-40 to +125	I to Y	10.0	-5.2	5.2
74HCT157	DIL, SO, SSOP,TSSOP	16	5V Quad 2-Input Multiplexer; TTL Enabled	Yes	TTL	5	-40 to +125	I to Y	16.0	-4	4
74HC158	DIL, SO	16	5V Quad 2-Input Multiplexer; Inverting	Yes	CMOS	5	-40 to +125	I to Y	12.0	-5.2	5.2
74HCT158	DIL, SO	16	5V Quad 2-Input Multiplexer; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	I to Y	16.0	-4	4
74HC251	DIL, SO, SSOP,TSSOP	16	5V 8-Input Multiplexer (3-State)	Yes	CMOS	5	-40 to +125	I to Y	14.0	-5.2	5.2
74HCT251	DIL, SO, SSOP,TSSOP	16	5V 8-Input Multiplexer (3-State); TTL Enabled	Yes	TTL	5	-40 to +125	I to Y	22.0	-4	4
74HC253	DIL, SO, SSOP	16	5V Dual 4-Input Multiplexer (3-State)	Yes	CMOS	5	-40 to +125	I to Y	16.0	-7.8	7.8
74HCT253	DIL, SO, SSOP	16	5V Dual 4-Input Multiplexer; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	I to Y	20.0	-6	6
74HC257	DIL, SO, SSOP,TSSOP	16	5V Quad 2-Input Multiplexer (3-State)	Yes	CMOS	5	-40 to +125	I to Y	10.0	-7.8	7.8
74HCT257	DIL, SO, SSOP,TSSOP	16	5V Quad 2-Input Multiplexer; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	I to Y	16.0	-6	6
74HC258	DIL, SO, SSOP	16	5V Quad 2-Input Multiplexer; Inverting (3-State)	Yes	CMOS	5	-40 to +125	I to Y	9.0	-7.8	7.8
74HCT258	DIL, SO	16	5V Quad 2-Input Multiplexer; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	I to Y	16.0	-6	6
74LV153	DIL, SO, SSOP,TSSOP	16	3.3V Dual 4-Input Multiplexer	Yes	TTL	3.3	-40 to +125	A to Y	16.0	-6	6
74LV251	DIL, SO, SSOP,TSSOP	16	3.3V 8-Input Multiplexer (3-State)	Yes	TTL	3.3	-40 to +125	I to Y	17.0	-6	6
74LVC157A	SO, SSOP,TSSOP, DQFN	16	3.3V Quad 2-Input Multiplexer	Yes	TTL	3.3	-40 to +125	I to Y	2.6	-24	24
74LVC257A	SO, SSOP,TSSOP, DQFN	16	3.3V Quad 2-Input Multiplexer (3-State)	Yes	TTL	3.3	-40 to +125	I to Y	2.9	-24	24

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC123A	SO,TSSOP	16	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input	Yes	CMOS	5	-40 to +125	A to Q	7.3	-8	8
74AHCT123A	SO,TSSOP	16	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input;TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	7.1	-8	8
74HC123	DIL, SO, SSOP,TSSOP	16	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input	Yes	CMOS	5	-40 to +125	B to Q	24.0	-5.2	5.2
74HCT123	DIL, SO, SSOP,TSSOP	16	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input;TTL Enabled	Yes	TTL	5	-40 to +125	B to Q	30.0	-4	4
74HC221	DIL, SO, SSOP	16	5V Dual Non-Retriggerable Monostable Multivibrator with Reset	Yes	CMOS	5	-40 to +125	B to Q	21.0	-5.2	5.2
74HCT221	DIL, SO, SSOP	16	5V Dual Non-Retriggerable Monostable Multivibrator with Reset; TTL Enabled	Yes	TTL	5	-40 to +125	B to Q	30.0	-4	4
74HC423	DIL, SO	16	5V Dual Retriggerable Monostable Multivibrator with Reset	Yes	CMOS	5	-40 to +125	A to Q	23.0	-5.2	5.2
74HCT423	DIL, SO, SSOP,TSSOP	16	5V Dual Retriggerable Monostable Multivibrator with Reset; TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	30.0	-4	4
74HC4538	DIL, SO, SSOP,TSSOP	16	5V Dual Retriggerable Precision Monostable Multivibrator	Yes	CMOS	5	-40 to +125	A to Q	25.0	-5.2	5.2
74HCT4538	DIL, SO, SSOP,TSSOP	16	5V Dual Retriggerable Precision Monostable Multivibrator; TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	35.0	-4	4
HEF4047B	DIL, SO	14	Monostable/Astable Multivibrator	Yes	CMOS	15	-40 to +125	Trig to O	50.0	-3	3
HEF4528B	DIL, SO	16	Dual Retriggerable Monostable Multivibrator with Reset	Yes	CMOS	15	-40 to +125	I to O	40.0	-3	3
HEF4538B	DIL, SO	16	Dual Retriggerable Precision Monostable Multivibrator	Yes	CMOS	15	-40 to +125	I to O	60.0	-3	3
74LV123	DIL, SO, SSOP,TSSOP	16	3.3V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input	Yes	TTL	3.3	-40 to +125	A to Q	25.0	-12	12

Parity Generator Checkers

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74ABT657	DIL, SO, SSOP,TSSOP	24	5V Octal Transceiver with Parity Generator/Checker; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	3.3	-32	64
74ABT899	SO, SSOP, PLCC	28	5V Latched Transceiver with Parity Generator/Checker (3-State)	No	TTL	5	-40 to +85	A to B	3.2	-32	64
74F280B	DIL, SO	14	5V 9-Bit Odd/Even Parity Generator/Checker	No	TTL	5	0 to +70	I to Sum	7.0	-1	20
74F657	DIL, SO	24	5V Transceiver with Parity Generator/Checker (3-State)	No	TTL	5	0 to +70	A to B	6.0	-15	48
74HC280	DIL, SO	14	5V 9-Bit Odd/Even Parity Generator/Checker	Yes	CMOS	5	-40 to +125	I to sum	16.0	-5.2	5.2
74HCT280	DIL, SO, SSOP	14	5V 9-Bit Odd/Even Parity Generator/Checker; TTL Enabled	Yes	TTL	5	-40 to +125	I to sum	21.0	-4	4

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74HC4046A	DIL, SO, SSOP, TSSOP	16	5V PLL with VCO	Yes	CMOS	5	-40 to +125	Sin to PC1	18.0	-5.2	5.2	17
74HCT4046A	DIL, SO, SSOP	16	5V PLL with VCO; TTL Enabled	Yes	TTL	5	-40 to +125	Sin to PC1	23.0	-4	4	17
74HC7046A	DIL, SO, SSOP	16	5V PLL with Lock Detector	Yes	CMOS	5	-40 to +125	Sin to PC1	17.0	-5.2	5.2	17
74HCT7046A	DIL, SO	16	5V PLL with Lock Detector; TTL Enabled	Yes	TTL	5	-40 to +125	Sin to PC1	21.0	-4	4	17
74HCT9046A	DIL, SO, TSSOP	16	5V PLL with Bandgap Controlled VCO; TTL Enabled	Yes	TTL	5	-40 to +125	Sin to PC1	23.0	-4	4	
HEF4046B	DIL, SO	16	PLL with VCO	Yes	CMOS	15	-40 to +125	VCO		-3	3	2.7

Printer Interfaces

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74LVC1284	SO, SSOP, TSSOP	20	3.3V Parallel Printer Interface Transceiver/Buffer	Yes	TTL	3.3	-40 to +125	A to Y	12.6	-14	14

Register Drivers

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74ALVCH16832	TSSOP	64	3.3V 7-Bit to 28-Bit Address Register/Driver (3-State)	Yes	TTL	3.3	-40 to +85	A to Y	2.9	-24	24	175
74F399	DIL, SO	16	5V Quad 2-Port Register	No	TTL	5	0 to +70	Cp to Q	6.5	-1	20	120
74HC670	DIL, SO, SSOP	16	5V 4 x 4 Register File (3-State)	Yes	CMOS	5	-40 to +125	R to Q	17.0	-7.8	7.8	
74HCT670	DIL, SO, SSOP	16	5V 4 x 4 Register File; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	R to Q	21.0	-6	6	
74HC7030	DIL, SO	28	5V 9-Bit x 64-Word FIFO Register (3-State)	Yes	CMOS	5	-40 to +125	So to Q	33.0	-5.2	5.2	30
74HCT7030	DIL, SO	28	5V 9-Bit x 64-Word FIFO Register (3-State); TTL Enabled	Yes	TTL	5	-40 to +125	So to Q	46.0	-4	4	26
74HC7403	DIL, SO	16	5V 4-Bit x 64-Word FIFO Register (3-State)	Yes	CMOS	5	-40 to +125	So to Q	30.0	-10	10	30
74HCT7403	DIL, SO	16	5V 4-Bit x 64-Word FIFO Register; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	So to Q	42.0	-8	8	30
74HC40105	DIL, SO, SSOP, TSSOP	16	5V 4-Bit x 16-Word FIFO Register	Yes	CMOS	5	-40 to +125	So to Q	34.0	-5.2	5.2	18
74HCT40105	DIL, SO, SSOP	16	5V 4-Bit x 16-Word FIFO Register; TTL Enabled	Yes	TTL	5	-40 to +125	So to Q	40.0	-4	4	28

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}
74AHC1G14	PicoGate	5	5V Single Inverting Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT1G14	PicoGate	5	5V Single Inverting Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.9	-8	8
74AHC3G14	TSSOP,VSSOP	8	5V Triple Inverting Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	4.6	-8	8
74AHCT3G14	TSSOP,VSSOP	8	5V Triple Inverting Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.9	-8	8
74AHC14	SO,TSSOP,DQFN	14	5V Hex Inverter Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	4.2	-8	8
74AHCT14	SO,TSSOP,DQFN	14	5V Hex Inverter Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.4	-8	8
74AHC132	SO,TSSOP	14	5V Quad 2-Input NAND Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	4.7	-8	8
74AHCT132	SO,TSSOP	14	5V Quad 2-Input NAND Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	5.0	-8	8
74ALVC14	SO,TSSOP,DQFN	14	3.3V Hex Inverting Schmitt-Trigger	Yes	TTL	3.3	-40 to +85	A to Y	2.4	-24	24
74F14	DIL, SO, DIL, SO	14	5V Hex Inverter Schmitt-Trigger	No	TTL	5	0 to +70	D to Q	6.5	-1	20
74F132	DIL, SO	14	5V Quad 2-Input NAND Schmitt-Trigger	No	TTL	5	0 to +70	D to Q	6.0	-1	20
74HC1G14	PicoGate	5	5V Single Inverting Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	11.0	-2.6	2.6
74HCT1G14	PicoGate	5	5V Single Inverting Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	17.0	-2	2
74HC3G14	TSSOP,VSSOP	8	5V Triple Inverting Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	13.0	-5.2	5.2
74HCT3G14	TSSOP,VSSOP	8	5V Triple Inverting Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	21.0	-4	4
74HC14	DIL, SO, SSOP,TSSOP,DQFN	14	5V Hex Inverter Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	13.0	-5.2	5.2
74HCT14	DIL, SO, SSOP,TSSOP,DQFN	14	5V Hex Inverter Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	21.0	-4	4
74HC132	DIL, SO, SSOP,TSSOP	14	5V Quad 2-Input NAND Schmitt-Trigger	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2
74HCT132	DIL, SO, SSOP,TSSOP	14	5V Quad 2-Input NAND Schmitt-Trigger; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	20.0	-4	4
74HC7014	DIL, SO	14	5V Hex Precision Schmitt-Trigger; Non-Inverting	Yes	CMOS	5	-40 to +125	A to Y	27.0	-5.2	5.2
74HC7540	DIL, SO, SSOP	20	5V Octal Schmitt-Trigger Buffer/Line Driver; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	11.0	-7.8	7.8
74HCT7540	DIL, SO	20	5V Octal Schmitt-Trigger Buffer/Line Driver; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	19.0	-6	6
74HC7541	DIL, SO, SSOP	20	5V Octal Schmitt-Trigger Buffer/Line Driver; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to Y	11.0	-7.8	7.8
74HCT7541	DIL, SO	20	5V Octal Schmitt-Trigger Buffer/Line Driver; Non-Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to Y	19.0	-6	6
74HC9114	DIL, SO	20	5V Nine Wide Schmitt-Trigger Buffer; Open Drain Outputs; Inverting	Yes	CMOS	5	-40 to +125	A to Y	10.0	-5.2	5.2
74HCT9114	DIL, SO	20	5V Nine Wide Schmitt-Trigger Buffer; Open Drain Outputs; Inverting; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	17.0	-4	4
74HC9115	DIL, SO	20	5V Nine Wide Schmitt-Trigger Buffer; Open Drain Outputs	Yes	CMOS	5	-40 to +125	A to Y	10.0	OD	5.2
74HCT9115	DIL, SO	20	5V Nine Wide Schmitt-Trigger Buffer; Open Drain Outputs; TTL Enabled	Yes	TTL	5	-40 to +125	A to Y	18.0	OD	4
HEF4093B	DIL, SO	14	Quad 2-Input NAND Schmitt-Trigger	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
HEF40106B	DIL, SO, TSSOP	14	Hex Inverting Schmitt-Trigger	Yes	CMOS	15	-40 to +125	I to O	30.0	-3	3
74LV14	DIL, SO, SSOP,TSSOP	14	3.3V Hex Inverter Schmitt-Trigger	Yes	TTL	3.3	-40 to +125	A to Y	15.0	-12	12
74LV132	DIL, SO, SSOP,TSSOP	14	3.3V Quad 2-Input NAND Schmitt-Trigger	Yes	TTL	3.3	-40 to +125	A to Y	12.0	-12	12
74LVC1G14	PicoGate	5	3.3V Single Schmitt-Trigger Inverter	Yes	TTL	3.3	-40 to +125	A to Y	3.0	-32	32
74LVC1G17	PicoGate	5	3.3V Single Schmitt-Trigger Buffer	Yes	TTL	3.3	-40 to +125	A to Y	3.0	-32	32
74LVC2G14	PicoGate	6	3.3V Dual Schmitt-Trigger Inverter	Yes	TTL	3.3	-40 to +125	A to Y	3.9	-32	32
74LVC2G17	PicoGate	6	3.3V Dual Schmitt-Trigger Buffer	Yes	TTL	3.3	-40 to +125	A to Y	3.6	-32	32
74LVC3G14	TSSOP,VSSOP	8	3.3V Triple Inverting Schmitt-Trigger	Yes	TTL	3.3	-40 to +125	A to Y	3.2	-32	32
74LVC3G17	TSSOP,VSSOP	8	3.3V Triple Non-Inverting Schmitt-Trigger with 5V-Tolerant Input	Yes	LVTTTL	3.3	-40 to +125	A to Y	3.6	-32	32
74LVC14A	SO, SSOP,TSSOP,DQFN	14	3.3V Hex Inverter Schmitt-Trigger	Yes	TTL	3.3	-40 to +125	A to Y	3.2	-24	24
74LVT14	SO, SSOP,TSSOP	14	3.3V Hex Inverter Schmitt-Trigger	No	TTL	3.3	-40 to +85	A to Y	3.8	-20	32

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74AHC164	SO,TSSOP	14	5V 8-Bit Serial-In/Parallel-Out Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	6.4	-8	8	115
74AHCT164	SO,TSSOP	14	5V 8-Bit Serial-In/Parallel-Out Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	4.9	-8	8	115
74AHC595	SO,TSSOP	16	5V 8-Bit Shift Register with Output Latches (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	5.4	-8	8	170
74AHCT595	SO,TSSOP	16	5V 8-Bit Shift Register with Output Latches; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	Cp to Q	5.2	-8	8	170
74F164	DIL, SO	14	5V 8-Bit Serial-In/Parallel-Out Shift Register	No	TTL	5	0 to +70	Cp to Q	7.0	-1	20	100
74F166	DIL, SO	16	5V 8-Bit Bidirectional Universal Shift Register	No	TTL	5	0 to +70	Cp to Q	7.5	-1	20	175
74F194	DIL, SO	16	5V 4-Bit Bidirectional Universal Shift Register	No	TTL	5	0 to +70	Cp to Q	5.5	-1	20	150
74F299	DIL, SO	20	5V 8-Bit Universal Shift/Store Register (3-State)	No	TTL	5	0 to +70	Cp to Q	6.0	-3	24	115
74HC164	DIL, SO, SSOP,TSSOP	14	5V 8-Bit Serial-In/Parallel-Out Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	12.0	-5.2	5.2	71
74HCT164	DIL, SO, SSOP,TSSOP	14	5V 8-Bit Serial-In/Parallel-Out Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	17.0	-4	4	55
74HC165	DIL, SO, SSOP,TSSOP	16	5V 8-Bit Parallel-In/Serial-Out Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-5.2	5.2	51
74HCT165	DIL, SO, SSOP,TSSOP	16	5V 8-Bit Parallel-In/Serial-Out Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	17.0	-4	4	44
74HC166	DIL, SO, SSOP,TSSOP	16	5V 8-Bit Bidirectional Universal Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	57
74HCT166	DIL, SO, SSOP	16	5V 8-Bit Bidirectional Universal Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	23.0	-4	4	45
74HC194	DIL, SO, SSOP	16	5V 4-Bit Bidirectional Universal Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	93
74HCT194	DIL, SO	16	5V 4-Bit Bidirectional Universal Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-4	4	70
74HC299	DIL, SO, SSOP	20	5V 8-Bit Universal Shift/Store Register (3-State)	Yes	CMOS	5	-40 to +125	Cp to I/O	19.0	-5.2	5.2	45
74HCT299	DIL, SO, SSOP	20	5V 8-Bit Universal Shift/Store Register (3-State); TTL Enabled	Yes	TTL	5	-40 to +125	Cp to I/O	22.0	-4	4	42
74HC594	DIL, SO, SSOP	16	5V 8-Bit Shift Register with Output Register	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-7.8	7.8	92
74HCT594	DIL, SO	16	5V 8-Bit Shift Register with Output Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	18.0	-6	6	92
74HC595	DIL, SO, SSOP,TSSOP, DQFN	16	5V 8-Bit Shift Register with Output Latches (3-State)	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-7.8	7.8	91
74HCT595	DIL, SO, SSOP,TSSOP, DQFN	16	5V 8-Bit Shift Register with Output Latches (3-State); TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	24.0	-6	6	52
74HC597	DIL, SO, SSOP,TSSOP	16	5V 8-Bit Shift Register with Input Storage Registers	Yes	CMOS	5	-40 to +125	Cp to Q	16.0	-5.2	5.2	87
74HCT597	DIL, SO, SSOP	16	5V 8-Bit Shift Register with Input Storage Registers; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	25.0	-4	4	75
74HC4015	DIL, SO, SSOP	16	5V Dual 4-Bit Serial-In/Parallel-Out Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-5.2	5.2	100
74HCT4015	DIL, SO	16	5V Dual 4-Bit Serial-In/Parallel-Out Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	21.0	-4	4	67
74HC4094	DIL, SO, SSOP,TSSOP	16	5V 8-Stage Shift-and-Store Bus Register	Yes	CMOS	5	-40 to +125	Cp to Q	14.0	-5.2	5.2	87
74HCT4094	DIL, SO, SSOP	16	5V 8-Stage Shift-and-Store Bus Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	23.0	-4	4	80
74HC7731	DIL, SO	16	5V Quad 64-Bit Static Shift Register	Yes	CMOS	5	-40 to +125	Cp to Q	15.0	-5.2	5.2	78
74HCT7731	DIL, SO	16	5V Quad 64-Bit Static Shift Register; TTL Enabled	Yes	TTL	5	-40 to +125	Cp to Q	24.0	-4	4	80
HEF4014B	DIL, SO	16	8-Bit Static Shift Register with Synchronous Parallel Enable Input	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	30
HEF4015B	DIL, SO	16	Dual 4-Bit Serial-In/Parallel-Out Shift Register	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	30
HEF4021B	DIL, SO	16	8-Bit Static Shift Register with Asynchronous Parallel Load Input	Yes	CMOS	15	-40 to +125	Cp to O	40.0	-3	3	30
HEF4094B	DIL, SO	16	8-Stage Shift-and-Store Bus Register	Yes	CMOS	15	-40 to +125	Cp to O	50.0	-3	3	14
HEF4517B	DIL, SO	16	Dual 64-Bit Static Shift Register	Yes	CMOS	15	-40 to +125	Cp to O	60.0	-3	3	16
HEF4557B	DIL, SO	16	1-to-64 Bit Variable Length Shift Register	Yes	CMOS	15	-40 to +125	Cp to O	65.0	-3	3	20
HEF4794B	DIL, SO	16	8-Stage Shift-and-Store Register LED Driver	Yes	CMOS	15	-40 to +125	Cp to O	55.0	OD	20	28
HEF4894B	DIL, SO	20	12-Stage Shift-and-Store Register LED Driver	Yes	CMOS	15	-40 to +125	Cp to O	55.0	OD	20	28
74LV164	DIL, SO, SSOP,TSSOP	14	3.3V 8-Bit Serial-In/Parallel-Out Shift Register	Yes	TTL	3.3	-40 to +125	Cp to Q	14.0	-12	12	100
74LV165	DIL, SO, SSOP,TSSOP	16	3.3V 8-Bit Parallel-In/Serial-Out Shift Register	Yes	TTL	3.3	-40 to +125	Cp to Q	22.0	-12	12	75
74LV165A	SO,TSSOP	16	3.3V 8-Bit Parallel-In/Serial-Out Shift Register with 5V Tolerant Inputs	Yes	TTL	3.3	-40 to +125	Cp to Q	7.5	-12	12	125
74LV595	DIL, SO, SSOP,TSSOP	16	3.3V 8-Bit Serial-In/Serial or Parallel-Out Shift Register with Output Latches (3-State)	Yes	TTL	3.3	-40 to +125	Cp to Q	18.0	-8	8	
74LV4094	DIL, SO, SSOP,TSSOP	16	3.3V 8-Stage Shift-and-Store Bus Register	Yes	TTL	3.3	-40 to +125	Cp to Q	17.0	-6	6	87

Timers

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74HC555	DIL, SO	16	5V Programmable Delay Timer with Oscillator	Yes	CMOS	5	-40 to +125	A to Q	22.0	-20	25	75
74HCT555	DIL, SO	16	5V Programmable Delay Timer with Oscillator; TTL Enabled	Yes	TTL	5	-40 to +125	A to Q	28.0	-20	20	75
HEF4541B	DIL, SO	14	Programmable Timer	Yes	CMOS	15	-40 to +125	RS to O	110.0	-1.2	2.7	36
74LV4799	DIL, SO, SSOP,TSSOP	16	3.3V Timer for NiCd and NiMH Chargers	Yes	TTL	3.3	-40 to +125	na	na	-6.1	6.1	

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74ABT245	SO, SSOP, TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	2.9	-32	64	
74ABT2245	SO, SSOP, TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to B	3.9	-32	12	
74ABT543A	DIL, SO, SSOP, TSSOP	24	5V Latched Transceiver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	3.6	-32	64	
74ABT544	DIL, SO, SSOP, TSSOP	24	5V Latched Transceiver; Inverting (3-State)	No	TTL	5	-40 to +85	A to B	3.9	-32	64	
74ABT620	DIL, SO, SSOP, TSSOP	20	5V Transceiver with Dual Enable; Inverting (3-State)	No	TTL	5	-40 to +85	A to B	3.1	-32	64	
74ABT623	DIL, SO, SSOP, TSSOP	20	5V Transceiver with Dual Enable; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	2.7	-32	64	
74ABT640	DIL, SO, SSOP, TSSOP	20	5V Transceiver with Direction Pin; Inverting (3-State)	No	TTL	5	-40 to +85	A to B	3.1	-32	64	
74ABT646A	DIL, SO, SSOP, TSSOP	24	5V Transceiver/Register (3-State)	No	TTL	5	-40 to +85	A to B	3.7	-32	64	350
74ABT648	SO, TSSOP	24	5V Octal Transceiver/Register; Inverting (3-State)	No	TTL	5	-40 to +85	C _p to A	5.9	-32	64	200
74ABT651	DIL, SO, TSSOP	24	5V Octal Transceiver/Register; Inverting (3-State)	No	TTL	5	-40 to +85	C _p to A	4.4	-32	64	300
74ABT652A	DIL, SO, SSOP, TSSOP	24	5V Transceiver/Register; Non-Inverting (3-State)	No	TTL	5	-40 to +85	C _p to A	4.3	-32	64	300
74ABT833	SO, SSOP, TSSOP	24	5V Octal Transceiver with Parity Generator/Checker (3-State)	No	TTL	5	-40 to +85	A to B	3.4	-32	64	
74ABT863	DIL, SO, SSOP, TSSOP	24	5V 9-Bit Bus Transceiver (3-State)	No	TTL	5	-40 to +85	A to B	3.4	-32	64	
74ABT2952	DIL, SO, SSOP, TSSOP	24	5V Registered Transceiver (3-State)	No	TTL	5	-40 to +85	C _p to A	3.8	-32	64	250
74ABT162245A	SSOP, TSSOP	48	5V 16-Bit Transceiver with Direction Pin; Non-Inverting with 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.0	-32	12	
74ABTH162245A	SSOP, TSSOP	48	5V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	5	-40 to +85	A to Y	3.0	-32	12	
74ABT16245B	SSOP, TSSOP	48	5V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	2.3	-32	64	
74ABTH16245B	SSOP	48	5V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	No	TTL	5	-40 to +85	A to B	2.3	-32	64	
74ABT16501A	SSOP, TSSOP	56	5V 18-Bit Universal Bus Transceiver; Positive-Edge Trigger Clock (3-State)	No	TTL	5	-40 to +85	A to B	2.2	-32	64	225
74ABT16543	SSOP, TSSOP	56	5V 16-Bit Latched Transceiver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	A to B	2.5	-32	64	
74ABT16646	SSOP, TSSOP	56	5V 16-Bit Transceiver/Register (3-State)	No	TTL	5	-40 to +85	A to B	3.3	-32	64	
74ABTH16646	SSOP, TSSOP	56	5V 16-Bit Transceiver/Register with Bus Hold (3-State)	No	TTL	5	-40 to +85	A to B	3.3	-32	64	
74ABT16652	SSOP, TSSOP	56	5V 16-Bit Transceiver/Register; Non-Inverting with (3-State)	No	TTL	5	-40 to +85	A to B	2.3	-32	64	
74ABT16952	SSOP, TSSOP	56	5V 16-Bit Registered Transceiver (3-State)	No	TTL	5	-40 to +85	C _p to A	2.8	-32	64	150
74AHC245	SO, TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to B	5.0	-8	8	
74AHCT245	SO, TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	6.0	-8	8	
74ALVC245	SO, TSSOP	20	3.3V Octal Transceiver with Direction Pin, Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.3	-24	24	
74ALVC16245	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	A to B	1.9	-24	24	
74ALVCH16245	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	1.9	-24	24	
74ALVCH162245	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.4	-12	12	
74ALVCH16500	TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Negative Edge Trigger Clock with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.9	-24	24	340
74ALVCH16501	TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	3.0	-24	24	333
74ALVCH16543	TSSOP	56	3.3V 16-Bit Latched Transceiver; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	3.8	-24	24	
74ALVCH16600	TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Negative Edge Trigger Clock with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.8	-24	24	362
74ALVCH16601	TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.9	-24	24	340
74ALVCH162601	TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold and 30 Ω Termination Resistors; (3-State)	Yes	TTL	3.3	-40 to +85	A to B	3.1	-12	12	240

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74ALVCH16623	TSSOP	48	3.3V 16-Bit Transceiver with Dual Enable; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.6	-24	24	
74ALVCH16646	TSSOP	56	3.3V 16-Bit Transceiver/Register with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.3	-24	24	320
74ALVCH16652	TSSOP	56	3.3V 16-Bit Transceiver/Register; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	2.6	-24	24	320
74ALVCH16952	TSSOP	56	3.3V 16-Bit Registered Transceiver with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	D to Q	3.2	-24	24	350
74ALVCH32501	LFBGA	114	3.3V 36-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	A to B	3.0	-24	24	340
74ALVT16245	SSOP,TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.5	-32	64	
74ALVT162245	SSOP,TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	A to B	2.3	-12	12	
74ALVT16501	SSOP,TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive-Edge Trigger Clock with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.8	-32	64	
74ALVT16543	SSOP,TSSOP	56	3.3V 16-Bit Latched Transceiver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.8	-32	64	
74ALVT16601	SSOP,TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive Edge-Trigger Clock with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.9	-32	64	
74ALVT16646	SSOP,TSSOP	56	3.3V 16-Bit Transceiver/Register with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.8	-32	64	
74ALVT16652	SSOP,TSSOP	56	3.3V 16-Bit Transceiver/Register; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	A to B	1.6	-32	64	
74AVC16245	TSSOP	48	2.5V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	CMOS	2.5	-40 to +85	A to B	1.3	-12	12	
74AVCH16245	TSSOP	48	2.5V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	Yes	CMOS	2.5	-40 to +85	A to B	1.6	-12	12	
74F242	DIL, SO	14	5V Quad Transceiver; Inverting (3-State)	No	TTL	5	0 to +70	A to B	3.5	-15	64	
74F243	DIL, SO	14	5V Quad Transceiver (3-State)	No	TTL	5	0 to +70	A to B	4.0	-15	64	
74F245	DIL, SO, SSOP	20	5V Transceiver with Direction Pin; Non-Inverting (3-State)	No	TTL	5	0 to +70	A to B	4.0	-15	64	
74F543	DIL, SO, SSOP	24	5V Latched Transceiver; Non-Inverting (3-State)	No	TTL	5	0 to +70	A to B	5.5	-15	64	
74F545	DIL, SO	20	5V Bidirectional Transceiver (3-State inputs/outputs)	No	TTL	5	0 to +70	A to B	4.5	-15	64	
74F640	DIL, SO	20	5V Transceiver with Direction Pin; Inverting (3-State)	No	TTL	5	0 to +70	A to B	4.5	-15	64	
74F862	DIL, SO	24	5V 10-Bit Transceiver (3-State)	No	TTL	5	0 to +70	A to B	6.0	-24	64	
74HC243	DIL, SO, SSOP	14	5V Quad Transceiver (3-State)	Yes	CMOS	5	-40 to +125	A to B	6.0	-7.8	7.8	
74HCT243	DIL, SO, SSOP	14	5V Quad Transceiver; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	13.0	-6	6	
74HC245	DIL, SO, SSOP,TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to B	7.0	-7.8	7.8	
74HCT245	DIL, SO, SSOP,TSSOP	20	5V Transceiver with Direction Pin; Non-Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	12.0	-6	6	
74HC640	DIL, SO, SSOP	20	5V Transceiver with Direction Pin; Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to B	9.0	-7.8	7.8	
74HCT640	DIL, SO, SSOP	20	5V Transceiver with Direction Pin; Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	11.0	-6	6	
74HC646	DIL, SO, SSOP	24	5V Transceiver/Register (3-State)	Yes	CMOS	5	-40 to +125	A to B	11.0	-7.8	7.8	63
74HCT646	DIL, SO, SSOP	24	5V Transceiver/Register; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	16.0	-6	6	77
74HC652	DIL, SO, SSOP,TSSOP	24	5V Transceiver/Register; Non-Inverting (3-State)	Yes	CMOS	5	-40 to +125	A to B	13.0	-7.8	7.8	83
74HCT652	DIL, SO	24	5V Transceiver/Register; Non-Inverting; TTL Enabled (3-State)	Yes	TTL	5	-40 to +125	A to B	16.0	-6	6	83
74LV245	DIL, SO, SSOP,TSSOP	20	3.3V Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	B to A	9.0	-16	16	
74LVC245A	SO, SSOP,TSSOP, DQFN	20	3.3V Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	B to A	2.9	-24	24	
74LVCH245A	SO, SSOP,TSSOP, DQFN	20	3.3V Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +125	B to A	2.9	-24	24	
74LVC2245A	SO, SSOP,TSSOP, DQFN	20	3.3V Transceiver with Direction Pin; Non-Inverting with 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +125	B to A	3.6	-12	12	
74LVC543A	SO, SSOP,TSSOP, DQFN	24	3.3V Latched Transceiver; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	B to A	3.3	-24	24	
74LVC544A	SO, SSOP,TSSOP	24	3.3V Latched Transceiver; Inverting (3-State)	Yes	TTL	3.3	-40 to +125	B to A	4.0	-24	24	

Type Number	Packages	Pins	Description	Low Power	Logic Level	V _{CC} Opt	Temperature Range °C	T _{PD} where	T _{PD} Opt	Drive I _{OH}	Drive I _{OL}	f _{MAX}
74LVC623A	SO, SSOP, TSSOP	20	3.3V Transceiver with Dual Enable; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +125	B to A	3.3	-24	24	
74LVC646A	SO, SSOP, TSSOP	24	3.3V Transceiver/Register (3-State)	Yes	TTL	3.3	-40 to +125	B to A	3.9	-24	24	
74LVC2952A	SO, SSOP, TSSOP	24	3.3V Registered Transceiver (3-State)	Yes	TTL	3.3	-40 to +125	C _p to Q	4.1	-24	24	150
74LVC16245A	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)	Yes	TTL	3.3	-40 to +85	B to A	2.2	-24	24	
74LVCH16245A	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	B to A	2.2	-24	24	
74LVC162245A	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	B to A	3.3	-12	12	
74LVCH162245A	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	B to A	3.3	-12	12	
74LVCH32245A	LFBGA	96	3.3V 32-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	Yes	TTL	3.3	-40 to +85	B to A	2.2	-24	24	
74LVCH322245A	LFBGA	96	3.3V 32-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	Yes	TTL	3.3	-40 to +85	B to A	3.3	-12	12	
74LVT245	SO, SSOP, TSSOP	20	3.3V Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	2.4	-32	64	
74LVT2245	SO, SSOP, TSSOP	20	3.3V Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	B to A	3.2	-12	12	
74LVT245B	SO, SSOP, TSSOP	20	3.3V Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	2.4	-32	64	
74LVT543	SO, SSOP, TSSOP	24	3.3V Latched Transceiver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	3.0	-32	64	
74LVT640	SO, SSOP, TSSOP	20	3.3V Transceiver with Direction Pin; Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	2.4	-32	64	
74LVT646	SO, SSOP, TSSOP	24	3.3V Transceiver/Register with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	3.8	-32	64	180
74LVT652	SO, SSOP, TSSOP	24	3.3V Transceiver/Register; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	3.7	-32	64	180
74LVT2952	SO, SSOP, TSSOP	24	3.3V Registered Transceiver with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	3.8	-32	64	200
74LVT16245B	SSOP, TSSOP, VFBGA	48, 48, 56	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	1.9	-32	64	
74LVT162245B	SSOP, TSSOP	48	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ω Termination Resistors (3-State)	No	TTL	3.3	-40 to +85	B to A	2.5	-12	12	
74LVT16500A	SSOP, TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Negative Edge Trigger Clock with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	3.2	-32	64	350
74LVT16501A	SSOP, TSSOP	56	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	3.2	-32	64	350
74LVT16543A	SSOP, TSSOP	56	3.3V 16-Bit Latched Transceiver; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	B to A	2.2	-32	64	
74LVT16646A	SSOP, TSSOP	56	3.3V 16-Bit Transceiver/Register with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	2.7	-32	64	180
74LVT16652A	SSOP, TSSOP	56	3.3V 16-Bit Transceiver/Register; Non-Inverting with Bus Hold (3-State)	No	TTL	3.3	-40 to +85	C _p to A	2.7	-32	64	180
MB2245	PQFP	52	5V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)	No	TTL	5	-40 to +85	B to A	3.2	-32	64	
MB2543	PQFP	52	5V 16-Bit Latched Transceiver; Non-Inverting (3-State)	No	TTL	5	-40 to +85	B to A	3.3	-32	64	
MB2652	PQFP	52	5V 16-Bit Transceiver/Register; Non-Inverting (3-State)	No	TTL	5	-40 to +85	C _p to A	4.4	-32	64	190